Name of the Proposal:

MiRitH (<u>MinR</u>ank <u>in the H</u>ead)

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1 Introduction

MIRITH (from <u>MinRank-in-the-Head</u>) is a digital signature scheme based on the hardness of the MinRank problem. Informally, the MinRank problem asks to find a non-trivial low-rank linear combination of some given matrices over a finite field. The construction of MIRITH starts from an MPC-in-the-Head (MPCitH) based Zero-Knowledge Proof of Knowledge (ZKPoK) of a solution to the MinRank problem, which is used to construct a 5-pass identification scheme. This identification protocol is then converted into a non-interactive signature scheme via the Fiat–Shamir transform.

1.1 Basic approach

In this section, we provide a rather informal overview of the techniques used by MIRITH.

The signature scheme is obtained from a 5-pass identification protocol via the Fiat–Shamir transform. In turn, the identification protocol follows the MPCitH paradigm. Therefore, to identify itself to a verifier, the prover simulates (in his head) an MPC protocol for N parties to prove knowledge about the solution to the MinRank problem. Later the prover reveals some states of this MPC execution to the verifier that allow him to check the validity of the execution and the result. The general phases of the identification protocol are outlined in Fig. 1.

Inputs: Prover and verifier know the problem instance, prover holds a witness for a solution to the problem.
Phase 1: Input preparation. The prover prepares all inputs for a total of N parties of an MPC protocol and commits to their initial states.
Phase 2: First Challenge. The verifier sends a first challenge, which later affects the computations within the MPC protocol.
Phase 3: MPC execution. The prover simulates all MPC parties based on their initial inputs and the first challenge. Subsequently, he commits to the intermediate and final states of the executions.
Phase 4: Second Challenge. The verifier sends a challenge i^{*} ∈ {1,...,N}.
Phase 5: Reveal Phase. The prover reveals the initial states of all MPC parties except for the i^{*}-th. Additionally, all intermediate states and the final state of party i^{*} are revealed.

Figure 1: Basic phases of the identification protocol of MIRITH.

Eventually, after receiving the initial states, the verifier can recompute all subsequent states of the MPC parties and check them against the commitments, for all parties except for the i^* -th party. Note that the verifier must not obtain knowledge of all initial states, since this would allow him to compute the solution to the MinRank problem. In general, this exclusion leads to a soundness probability of 1/N as guessing this challenge would allow cheating in the input preparation of the i^* -th party.¹ Also, by obtaining all the final states of the parties, the verifier can check that the MPC protocol execution was successful.

Concrete instantiations of the general MPCitH framework from Fig. 1, like MIRITH, are mainly characterized by the underlying problem and the used MPC protocol. Therefore, let us briefly

¹The effective soundness probability is slightly larger, which is related to the probability of guessing the first challenge. However, the first challenge space is sufficiently large such that the probability is exponentially close to 1/N.

sketch those characteristics for MIRITH.

1.1.1 Witness for a MinRank solution

We use the Kipnis–Shamir modeling [KS99], which essentially allows us to prove the knowledge of a MinRank solution α by a matrix-product identity

$$\boldsymbol{M}_0 = \boldsymbol{M}_1 \cdot \boldsymbol{K},\tag{1}$$

where M_0 and M_1 are specifically crafted linear combinations of the matrices defining the problem instance depending on the solution α , and K is another secret matrix. Put differently, if M_0 and M_1 are correctly constructed, proving that Eq. (1) holds is equivalent to proving knowledge to a solution to the MinRank problem. More details on this modeling are given in Section 2.1.1. The main advantage of using this modeling is that we obtain a matrix identity, which can be verified via an MPC protocol instead of a statement about the rank of a matrix.

1.1.2 MPC protocol

MIRITH uses a linear secret sharing scheme between N parties, where each party *i* holds an additive share α_i of the solution $\alpha = \sum_{i=1}^{N} \alpha_i$ and an additive share K_i of the matrix $K = \sum_{i=1}^{N} K_i$. Based on its shares, each party can construct its own share of M_0 and M_1 . More details on additive sharings are provided in Section 2.

From here, an MPC protocol for the verification of matrix-multiplication triples is used. A matrix-multiplication triple is a set of matrices (A, B, C) fulfilling $A \cdot B = C$.

MIRITH uses this MPC protocol to prove that the shares of all parties form a valid matrixmultiplication triple, i.e., that they satisfy Eq. (1) and, hence, that α forms a solution to the MinRank problem.

1.2 Comparison to other MinRank-based schemes

MIRITH is built on top of the MinRank-based signature scheme proposed by Adj, Rivera-Zamarripa, and Verbel [ARZV22]. In that work, Adj et al. introduce a Multi-Party Computation (MPC) protocol II to verify solutions to the MinRank problem using the Kipnis–Shamir modeling [KS99] and checking that a triple of shared matrices (Z, X, Y) satisfies $Z = X \cdot Y$.

MIRITH introduces two optimizations over [ARZV22], namely:

- It improves the protocol Π by employing an optimization analogous to the one introduced by Kales and Zarevucha [KZ22, Section 2.5]. Note that this optimization is also used in [Fen22];
- 2. It improves the protocol Π by reducing the size of a random matrix (later called R) used in the protocol, leveraging an optimization introduced by Feneuil [Fen22].

In the following we list other MinRank-based signature schemes in the literature and briefly explain how they differ from MIRITH.

• Courtois' scheme [Cou01]: To the best of our knowledge, this is the first signature scheme based on the hardness of the MinRank problem and already has a ring signature variant. Courtois' scheme, contrary to MIRITH, is derived directly from the Fiat–Shamir transform applied to a zero-knowledge sigma protocol with soundness error 2/3.

- MR-DSS [BESV22]: MR-DSS improves upon Courtois' scheme by using a sigma protocol with helper, which has a soundness error of 1/2. Also, the authors of MR-DSS formalized the ring signature version of the scheme. Our extension of MIRITH to ring signatures uses their formalization.
- Feneuil's schemes [Fen22]: Recently, Feneuil proposed two MinRank-based schemes, both using the MPCitH approach applied to an MPC protocol Π. These schemes differ from MIRITH in how they use Π to prove a solution to the MinRank problem.
 - 1. The first version uses the protocol Π to prove the knowledge of a linear combination of the given matrices that equals the product of two matrices, where the left factor has rcolumns, and the other one has r rows. In this case, the resulting product has the same dimension as the input matrices. MIRITH instead uses the Kipnis–Shamir modeling, which is slightly more efficient as it involves a matrix product with r fewer columns.
 - 2. The second protocol proposed by Feneuil instead proves that every row of the low-rank linear combination matrix represents an element of a field extension that is a root of a secret polynomial. This approach is completely different from that of MIRITH.

2 Notation

We list below some basic notations used throughout the paper:

- $[n] := \{1, 2, \dots, n\}$ for any positive integer n.
- \mathbb{F}_q denotes a finite field of q elements.
- $\mathbb{F}_q^{m \times n}$ is the vector space of $m \times n$ matrices over \mathbb{F}_q .
- $\mathsf{Rank}(M)$ is the rank of a matrix $M \in \mathbb{F}_q^{m \times n}$.
- 0 is a zero matrix (the size will always be clear from the context).
- \boldsymbol{M} denotes a tuple of k+1 matrices $(M_0, M_1, \ldots, M_k) \in (\mathbb{F}_q^{m \times n})^{k+1}$.

-
$$M_{\alpha} := M_0 + \sum_{i=1}^k \alpha_i M_i$$
 for each $\alpha = (\alpha_1, \dots, \alpha_k) \in \mathbb{F}_q^k$.

- λ denotes the security parameter.
- N is the number of parties of the MPC protocol.
- $a \leftarrow \mathcal{A}(x)$ means that a is the output of an algorithm \mathcal{A} on input x.
- $a \stackrel{\$}{\leftarrow} S$ means that a is sampled uniformly at random from a set S.
- log is the logarithm in base 2.

We also give the following relevant definitions:

• Left- and Right-Side of a Matrix. Let $A \in \mathbb{F}_q^{m \times n}$, and $1 \leq r < n$ be an integer. We define the *left-side* $A^{L_r} \in \mathbb{F}_q^{m \times (n-r)}$ and the *right-side* $A^{R_r} \in \mathbb{F}_q^{m \times r}$ of A, with respect to r, as the matrices satisfying

$$A = [A^{L_r} | A^{R_r}].$$

In the remainder of this specification, r always refers to the target rank of our MinRank instances. Hence, we write conveniently A^L and A^R .

- Additive sharing. Let a be an element in an additive group. We say that a tuple $\llbracket a \rrbracket := (\llbracket a \rrbracket_1, \ldots, \llbracket a \rrbracket_N)$ such that $a = \sum_{i=1}^N \llbracket a \rrbracket_i$ is an *additive sharing* of a.
- Distributed operations in Multi-Party Computation. Let \mathbb{V} be a vector space over a field \mathbb{F} . In the MPC context, an additive sharing is distributed between N parties, i.e., each party gets one of the N shares. Thus the parties can do distributed computation on the shares [x], [y] of $x, y \in \mathbb{V}$ as follows:
 - Addition. The parties locally compute [x + y] by adding their respective shares:

$$[x+y]_i := [x]_i + [y]_i \quad \forall i \in [N]$$

This process is denoted by $\llbracket x + y \rrbracket = \llbracket x \rrbracket + \llbracket y \rrbracket$.

- Addition with a constant. For a given constant $a \in \mathbb{V}$ the parties locally compute [x + a] as

$$\llbracket x+a \rrbracket_1 = \llbracket x \rrbracket_1 + a \quad \text{and} \quad \llbracket x+a \rrbracket_i = \llbracket x \rrbracket_i \quad \text{ for } i \in [N] \setminus \{1\}.$$

This process is denoted by [x + a] = [x] + a.

- Multiplication by a constant. For a given constant $a \in \mathbb{F}$ the parties locally compute $[\![a \cdot x]\!]$ as

$$\llbracket a \cdot x \rrbracket_i = a \cdot \llbracket x \rrbracket_i \quad \forall i \in [N].$$

This process is denoted by $\llbracket a \cdot x \rrbracket = a \cdot \llbracket x \rrbracket$.

• Matrix-multiplication triple. Let X, Y, Z be matrices such that $Z = X \cdot Y$. Then, we say that the triple of shares ([X], [Y], [Z]) is a matrix-multiplication triple.

2.1 MinRank

The MinRank problem is the underlying hard problem of the signature scheme that we propose.

Problem (MinRank). Let q,m,n,k, and r be positive integers, with q a prime power. The MinRank problem with parameters (q, m, n, k, r) is defined as:

Given: (k+1)-tuple $M = (M_0, M_1, \dots, M_k) \in (\mathbb{F}_q^{m \times n})^{k+1}$.

Find: $\boldsymbol{\alpha} = (\alpha_1, \dots, \alpha_k) \in \mathbb{F}_q^k$ such that $\mathsf{Rank}(M_0 + \sum_{i=1}^k \alpha_i M_i) \leq r$.

2.1.1 Kipnis–Shamir modeling

The Kipnis–Shamir modeling was introduced in 1999 [KS99] and is based on the following fact. If there is a vector $\boldsymbol{\alpha} \in \mathbb{F}_q^k$ and a matrix $K \in \mathbb{F}_q^{r \times (n-r)}$ such that

$$\left(M_0 + \sum_{i=1}^k \alpha_i M_i\right) \cdot \begin{bmatrix} I \\ -K \end{bmatrix} = \mathbf{0},\tag{2}$$

where $I \in \mathbb{F}_q^{(n-r) \times (n-r)}$ is a non-singular matrix, then the vector $\boldsymbol{\alpha} = (\alpha_1, \dots, \alpha_k)$ is a solution to the instance $\boldsymbol{M} = (M_0, M_1, \dots, M_k)$ of the MinRank problem.

If in Eq. (2) we fix I to be the identity matrix of size $(n-r) \times (n-r)$, then we get that

$$M_0^L + \sum_{i=1}^k \alpha_i M_i^L = \left(M_0^R + \sum_{i=1}^k \alpha_i M_i^R \right) \cdot K,$$
(3)

that is, $M_{\alpha}^{L} = M_{\alpha}^{R} \cdot K$. To solve an instance of the MinRank problem, Kipnis and Shamir proposed to solve the bilinear system having $\alpha_1, \ldots, \alpha_k$ and the entries of K as unknowns and the entries of Eq. (2) as equations.

3 Signature Scheme

3.1 Design rationale

MIRITH is based on the MPCitH paradigm. Therefore, it uses an MPC protocol to prove the knowledge of a solution to the MinRank problem.

3.1.1 MPC protocol

Inputs: Each party knows the MinRank instance M and takes a share of the following sharings as input: $[\![\alpha]\!]$ and $[\![K]\!]$, where $\alpha \in \mathbb{F}_q^k$ and $K \in \mathbb{F}_q^{r \times (n-r)}$; $[\![A]\!]$, where A has been uniformly sampled from $\mathbb{F}_q^{s \times r}$; and $[\![C]\!]$, where $C = A \cdot K$.

MPC Protocol:

- 1: The parties locally compute $\llbracket \boldsymbol{M}_{\boldsymbol{\alpha}}^{L} \rrbracket$ and $\llbracket \boldsymbol{M}_{\boldsymbol{\alpha}}^{R} \rrbracket$.
- 2: The parties get a random matrix $R \xleftarrow{\$} \mathbb{F}_q^{s \times m}$.
- 3: The parties locally set $\llbracket S \rrbracket = R \cdot \llbracket M_{\alpha}^{R} \rrbracket + \llbracket A \rrbracket$.
- 4: The parties open $\llbracket S \rrbracket$ so that they all obtain S.
- 5: The party locally compute $\llbracket V \rrbracket = S \cdot \llbracket K \rrbracket R \cdot \llbracket M_{\alpha}^{L} \rrbracket \llbracket C \rrbracket$.
- 6: The parties open $\llbracket V \rrbracket$ to obtain V.
- 7: The parties output **accept** if V = 0 and **reject** otherwise.

Figure 2: The MPC protocol Π_s to check that $M^L_{\alpha} = M^R_{\alpha} \cdot K$.

As discussed in Section 2.1.1, in our proof of knowledge, the witness of a solution to an instance M of the MinRank problem is a pair (α, K) consisting of a vector $\alpha \in \mathbb{F}_q^k$ and a matrix $K \in \mathbb{F}_q^{r \times (n-r)}$ such that $M_{\alpha}^L = M_{\alpha}^R \cdot K$. Let s be a positive integer. In Fig. 2, we describe an MPC protocol Π_s that checks the matrix identity $M_{\alpha}^L = M_{\alpha}^R \cdot K$ with soundness error $1/q^s$ (see Proposition 2).

3.1.2 ZKPoK for MinRank

 $\mathsf{ZKProof}(\mathsf{Prover}(\boldsymbol{M}, \boldsymbol{\alpha}, K), \mathsf{Verifier}(\boldsymbol{M}))$ **Phase 1:** Prover sets up the inputs for the MPC protocol Π_s : 1: seed $\stackrel{\$}{\leftarrow} \{0,1\}^{\lambda}$, (seed_i, ρ_i)_{i \in [N]} \leftarrow TreePRG(seed) 2: For each party $i \in [N-1]$ $\llbracket \boldsymbol{\alpha} \rrbracket_i, \llbracket K \rrbracket_i, \llbracket A \rrbracket_i, \llbracket C \rrbracket_i \leftarrow \operatorname{PRG}(\mathsf{seed}_i)$ $\mathsf{state}_i \leftarrow \mathsf{seed}_i$ $3: \quad [\![\boldsymbol{\alpha}]\!]_N \leftarrow \boldsymbol{\alpha} - \sum\nolimits_{i \neq N} [\![\boldsymbol{\alpha}]\!]_i, \quad [\![K]\!]_N \leftarrow K - \sum\nolimits_{i \neq N} [\![K]\!]_i$ 4: $\llbracket A \rrbracket_N \leftarrow \operatorname{PRG}(\operatorname{seed}_N), \quad \llbracket C \rrbracket_N \leftarrow A \cdot K - \sum_{i \neq N} \llbracket C \rrbracket_i$ 5: $\mathsf{aux} \leftarrow (\llbracket \alpha \rrbracket_N, \llbracket K \rrbracket_N, \llbracket C \rrbracket_N), \quad \mathsf{state}_N \leftarrow (\mathsf{seed}_N, \mathsf{aux})$ 6: Commit to each party's state: $\operatorname{com}_i \leftarrow \operatorname{Com}(\operatorname{state}_i, \rho_i)$, for all $i \in [N]$. 7:Prover computes $h_1 \leftarrow \text{Hash}_1(\text{com}_1, \dots, \text{com}_N)$ and sends it to Verifier. **Phase 2:** Verifier samples $R \stackrel{\$}{\leftarrow} \mathbb{F}_q^{s \times m}$ and sends it to Prover. **Phase 3:** Prover simulates the MPC protocol Π_s : The parties locally compute $\llbracket M_{\alpha}^{L} \rrbracket$ and $\llbracket M_{\alpha}^{R} \rrbracket$. 8: The parties locally set $[S] = R \cdot [M_{\alpha}^{R}] + [A].$ 9: The parties open $\llbracket S \rrbracket$ so that they all obtain S. 10:The parties locally compute $\llbracket V \rrbracket = S \cdot \llbracket K \rrbracket - R \cdot \llbracket M_{\alpha}^{L} \rrbracket - \llbracket C \rrbracket$. 11: Prover computes $h_2 \leftarrow \operatorname{Hash}_2(\llbracket S \rrbracket_1, \llbracket V \rrbracket_1, \dots, \llbracket S \rrbracket_N, \llbracket V \rrbracket_N)$. 12:Prover sends h_2 to Verifier. 13:**Phase 4:** Verifier samples $i^* \stackrel{\$}{\leftarrow} [N]$ and sends it to Prover. **Phase 5:** Prover sends $rsp := ((state_i, \rho_i)_{i \neq i^*}, com_{i^*}, [S]_{i^*})$ to Verifier. Verification: 14: Verifier recompute $(\llbracket S \rrbracket_i, \llbracket V \rrbracket_i)_{i \neq i^*}$ from $(\mathsf{state}_i)_{i \neq i^*}$. 15: $\llbracket V \rrbracket_{i^*} \leftarrow -\sum_{i \neq i^*} \llbracket V \rrbracket_i$ Verifier accepts if and only if $com_i = Com(state_i, \rho_i)$, for each $i \neq i^*$, 16: $h_1 = \text{Hash}_1(\text{com}_1, \dots, \text{com}_N), h_2 = \text{Hash}_2([\![S]\!]_1, [\![V]\!]_1, \dots, [\![S]\!]_N, [\![V]\!]_N)$

Figure 3: Zero-knowledge proof of knowledge for MinRank.

Starting from the MPC protocol Π_s , we build a ZKPoK of a solution to an instance of the MinRank problem as described in Fig. 3. This ZKPoK employs a commitment scheme Com, a

pseudorandom generator PRG, and a tree seed TreePRG (see Section 9.1 for details). The completeness, soundness, and honest-verifier zero-knowledge properties of the ZKPoK of Fig. 3 are provided in Section 9.

3.1.3 Non-interactive via the Fiat–Shamir transform

To transform the ZKPoK of Fig. 3 into a non-interactive signature scheme, we use a standard generalization of the Fiat–Shamir transform for canonical 5-pass protocols [FS87].

Precisely, for a message $\mathsf{msg} \in \{0,1\}^*$, the prover sample a random value $\mathsf{salt} \leftarrow \{0,1\}^{2\lambda}$ and then simulates τ rounds of the protocol of Fig. 3 as follows. For each round $\ell \in [\tau]$, the prover computes the commitments $\mathsf{com}_1^{(\ell)}, \ldots, \mathsf{com}_N^{(\ell)}$ as in the interactive protocol, and then produces the hash

$$h_1 = \operatorname{Hash}_1\left(\mathsf{msg},\mathsf{salt},(\mathsf{com}_i^{(\ell)})_{i\in[N],\ell\in[\tau]}\right)$$

and the first challenges $R^{(1)}, \ldots, R^{(\tau)} \leftarrow \operatorname{PRG}(h_1)$. Then, for each round $\ell \in [\tau]$, the prover computes the matrices $[\![S^{(\ell)}]\!]_1, [\![V^{(\ell)}]\!]_1, \ldots, [\![S^{(\ell)}]\!]_N, [\![V^{(\ell)}]\!]_N$ as in the interactive protocol. After that, the prover produces the hash

$$h_2 = \operatorname{Hash}_2\left(\mathsf{msg},\mathsf{salt},h_1,\left(\left[\!\left[S^{(\ell)}\right]\!\right]_i,\left[\!\left[V^{(\ell)}\right]\!\right]_i\right)_{i\in[N],\ell\in[\tau]}\right)$$

and the second challenges $i^{*,(1)}, \ldots, i^{*,(\tau)} \leftarrow \operatorname{PRG}(h_2)$. Finally, the prover uses the challenges $(i^{*,(\ell)})_{\ell \in [\tau]}$ to assemble the signature

$$\sigma = \left(\mathsf{salt}, h_1, h_2, \left(\left(\mathsf{state}_i^{(\ell)}\right)_{i \neq i^*, (\ell)}, \mathsf{com}_{i^*, (\ell)}^{(\ell)}, \left[\!\!\left[S^{(\ell)}\right]\!\!\right]_{i^*, (\ell)}\!\right)_{\ell \in [\tau]}\right)$$

which consists in revealing all the states of each round ℓ except for the $i^{*,(\ell)}$ -th state, for which $\operatorname{com}_{i^*,(\ell)}^{(\ell)}$ and $[S^{(\ell)}]_{i^*,(\ell)}$ are provided instead.

3.2 Key generation algorithm



Figure 4: Algorithms for the generation and the decompression of the public and secret keys.

The uncompressed public key and secret key of our scheme are a random instance M of the MinRank problem and the corresponding witness (α, K) , respectively. If stored directly, they would require $mnk \log q$ bits and $(k+r(n-r)) \log q$ bits, respectively. We employ the key generation scheme proposed by Adj et al. [ARZV22], which compresses the public key and the secret key into $\lambda + mn \log q$ and λ bits, respectively. See Fig. 4 for the details.

We remark that using the key generation scheme proposed by Di Scala and Sanna [DSS23] the public key could be compressed further to $\lambda + (m(n-r) - k) \log q$ bits, but at the cost of more computation time for the key generation.

3.3 Signing and verification algorithms

The signing algorithm is derived as the non-interactive version of the identification protocol outlined in Section 3.1.2 by applying the Fiat–Shamir transform (compare to Section 3.1.3). Thereby, to reach the necessary soundness, we use τ parallel repetitions. Pseudocodes are given by Fig. 5 and Fig. 6.

 $Sign(M, \alpha, K, msg)$ 1: salt $\stackrel{\$}{\leftarrow} \{0,1\}^{2\lambda}$ Phase 1: Set up the views for the MPC protocols for $\ell \in [\tau]$ do $\operatorname{seed}^{(\ell)} \xleftarrow{\$} \{0,1\}^{\lambda}, \quad (\operatorname{seed}_{i}^{(\ell)})_{i \in [N]} \leftarrow \operatorname{TreePRG}(\operatorname{salt}, \operatorname{seed}^{(\ell)})$ 2:for $i \in [N-1]$ do $\llbracket A^{(\ell)} \rrbracket_i, \llbracket \boldsymbol{\alpha}^{(\ell)} \rrbracket_i, \llbracket C^{(\ell)} \rrbracket_i, \llbracket K^{(\ell)} \rrbracket_i \leftarrow \mathrm{PRG}(\mathsf{salt}, \mathsf{seed}_i^{(\ell)})$ 3: $4: \qquad \mathsf{state}_i^{(\ell)} \gets \mathsf{seed}_i^{(\ell)}$ $5: \qquad [\![A^{(\ell)}]\!]_N, \leftarrow \mathrm{PRG}(\mathsf{salt}, \mathsf{seed}_N^{(\ell)}), \quad [\![\boldsymbol{\alpha}^{(\ell)}]\!]_N \leftarrow \boldsymbol{\alpha} - \sum_{i \neq N} [\![\boldsymbol{\alpha}^{(\ell)}]\!]_i$ $6: \qquad \llbracket K^{(\ell)} \rrbracket_N \leftarrow K - \sum_{i \neq N} \llbracket K^{(\ell)} \rrbracket_i, \quad \llbracket C^{(\ell)} \rrbracket_N \leftarrow A^{(\ell)} \cdot K - \sum_{i \neq N} \llbracket C^{(\ell)} \rrbracket_i$ 7: $\mathsf{aux}^{(\ell)} \leftarrow (\llbracket \boldsymbol{\alpha}^{(\ell)} \rrbracket_N, \llbracket K^{(\ell)} \rrbracket_N, \llbracket C^{(\ell)} \rrbracket_N), \quad \mathsf{state}_N^{(\ell)} \leftarrow (\mathsf{seed}_N^{(\ell)}, \mathsf{aux}^{(\ell)})$ 8: $\operatorname{com}_{i}^{(\ell)} \leftarrow \operatorname{Hash}_{0}(\operatorname{salt}, \ell, i, \operatorname{state}_{i}^{(\ell)}), \text{ for all } i \in [N]$ Phase 2: First challenges 9: $h_1 \leftarrow \operatorname{Hash}_1(\operatorname{\mathsf{msg}}, \operatorname{\mathsf{salt}}, (\operatorname{\mathsf{com}}_i^{(\ell)})_{i \in [N], \ell \in [\tau]})$ 10: $R^{(1)}, \ldots, R^{(\tau)} \leftarrow \operatorname{PRG}(h_1)$ Phase 3: Simulation of the MPC protocols for $\ell \in [\tau]$ do 11: Compute $\llbracket \boldsymbol{M}_{\boldsymbol{\alpha}}^{L,(\ell)} \rrbracket, \llbracket \boldsymbol{M}_{\boldsymbol{\alpha}}^{R,(\ell)} \rrbracket$ from $\llbracket \boldsymbol{\alpha}^{(\ell)} \rrbracket$ 12: $[S^{(\ell)}] \leftarrow R^{(\ell)} \cdot [\mathbf{M}^{R,(\ell)}] + [A^{(\ell)}]$ 13: $S^{(\ell)} \leftarrow \sum_{i} \llbracket S^{(\ell)} \rrbracket_{i}$ 14: $\llbracket V^{(\ell)} \rrbracket \leftarrow S^{(\ell)} \cdot \llbracket K^{(\ell)} \rrbracket - R^{(\ell)} \cdot \llbracket \boldsymbol{M}_{\boldsymbol{\alpha}}^{L,(\ell)} \rrbracket - \llbracket C^{(\ell)} \rrbracket$ Phase 4: Second challenges 15: $h_2 \leftarrow \operatorname{Hash}_2(\mathsf{msg}, \mathsf{salt}, h_1, (\llbracket S^{(\ell)} \rrbracket_i, \llbracket V^{(\ell)} \rrbracket_i)_{i \in [N] \ \ell \in [\tau]})$ 16: $i^{*,(1)}, \ldots, i^{*,(\tau)} \leftarrow \text{PRG}(h_2)$ Phase 5: Assembling the signature σ 17: $\sigma \leftarrow \left(\mathsf{salt}, h_1, h_2, \left(\left(\mathsf{state}_i^{(\ell)}\right)_{i \neq i^*, (\ell)}, \mathsf{com}_{i^*, (\ell)}^{(\ell)}, \llbracket S^{(\ell)} \rrbracket_{i^*, (\ell)}\right)_{\ell \in [\tau]}\right)$ return σ

Figure 5: Signing algorithm of MIRITH.

$$\begin{split} & \frac{\operatorname{Verif}(\boldsymbol{M}, \operatorname{msg}, \sigma)}{1: \quad R^{[1]}, \dots, R^{(\tau)} \leftarrow \operatorname{PRG}(h_1), \quad i^{*,[1]}, \dots, i^{*,(\tau)} \leftarrow \operatorname{PRG}(h_2) \\ & \text{ for all } \ell \in [\tau] \text{ do} \\ 2: \quad \operatorname{Compute } \operatorname{com}_i^{(\ell)} \text{ and } [\![S^{(\ell)}]\!]_i \text{ as in Sign}(), \text{ for all } i \in [N] \backslash \{i^{*,(\ell)}\} \\ 3: \quad S^{(\ell)} \leftarrow \sum_i [\![S^{(\ell)}]\!]_i \\ 4: \quad \operatorname{Compute } [\![V^{(\ell)}]\!]_i \text{ as in Sign}(), \text{ for all } i \in [N] \backslash \{i^{*,(\ell)}\} \\ 5: \quad [\![V^{(\ell)}]\!]_{i^{*,(\ell)}} \leftarrow -\sum_{i \neq i^{*,(\ell)}} [\![V^{(\ell)}]\!]_i \\ 6: \quad h'_1 \leftarrow \operatorname{Hash}_1(\operatorname{msg}, \operatorname{salt}, (\operatorname{com}_i^{(\ell)})_{i \in [N], \ell \in [\tau]}) \\ 7: \quad h'_2 \leftarrow \operatorname{Hash}_2(\operatorname{msg}, \operatorname{salt}, h_1, ([\![S^{(\ell)}]\!]_i, [\![V^{(\ell)}]\!]_i)_{i \in [N], \ell \in [\tau]}) \\ 8: \quad \operatorname{Output } \operatorname{accept } \operatorname{if } h'_1 = h_1 \text{ and } h'_2 = h_2, \text{ otherwise output reject} \end{split}$$

Figure 6: Verification algorithm of MIRITH.

3.4 Hypercube variant

The hypercube-MPCitH protocol, introduced by Aguilar-Melchor et al. [AMGH⁺23], allows one to reduce the number of parties to be simulated in an N^D -party classical MPCitH to only ND, without loss of soundness. To apply the hypercube approach to MIRITH, we set the number of parties to N^D , instead of N as in classical MIRITH. Then, the views for the N^D parties, called *leaf parties*, are set up and committed as in Phase 1 of Fig. 5. Next, each party $i \in [N^D]$ is expressed as $(i_1, \ldots, i_D) \in [N]^{D-2}$, and for every $k \in [D]$, we form N extra parties $\{(k, j) : j \in [N]\}$, called *main parties*. For a shared value X, the share $[\![X]\!]_{(k,j)}$ of a main party (k, j) will be the sum of the shares $[\![X]\!]_i$ of the leaf parties $i = (i_1, \ldots, i_D)$ having $i_k = j$. Now, the MPC simulation in Phase 3 of Fig. 5 is carried out here on each subset $\{(k, j) : j \in [N]\}$ $(k \in D)$ of the ND main parties. The hypercube variant of MIRITH, which we call MIRITH-HYPERCUBE, is described in Fig. 7 and Fig. 8.

The hypercube approach offers a two-fold advantage. First, the computational complexity is improved, since, besides having to simulate exponentially fewer parties as mentioned above, the N^D shares to set up can be essentially performed offline. Second, while executing D MPC protocols of size N each, only one auxiliary state is used for all of them, hence considerably reducing the communication cost. For further details and security analysis on the hypercube-MPCitH, we refer to the paper of Aguilar-Melchor et al. [AMGH⁺23].

²We represent *i* as (i_1, \ldots, i_D) , where the i_j 's are obtained by expressing i-1 in *N*-ary as $i-1 = i'_1 + i'_2 N + i'_3 N^2 + \ldots i'_N N^{D-1}$ and setting $i_j = i'_j + 1$ for every $j \in [N]$

SignHypercube(M, α, K, msg) 1: salt $\stackrel{\$}{\leftarrow} \{0,1\}^{2\lambda}$ Phase 1: Set up the views for the MPC protocols for $\ell \in [\tau]$ do $\operatorname{seed}^{(\ell)} \stackrel{\$}{\leftarrow} \{0,1\}^{\lambda}, \quad (\operatorname{seed}_{i}^{(\ell)})_{i \in [N^{D}]} \leftarrow \operatorname{TreePRG}(\operatorname{salt}, \operatorname{seed}^{(\ell)})$ 2: for $(k, j) \in [D] \times [N]$ do Initialize all entiries of $[\![A^{(\ell)}]\!]_{(k,j)}, [\![\alpha^{(\ell)}]\!]_{(k,j)}, [\![C^{(\ell)}]\!]_{(k,j)}, [\![K^{(\ell)}]\!]_{(k,j)}$ to zero. 3: for $i \in [N^D - 1]$ do $\llbracket A^{(\ell)} \rrbracket_i, \llbracket \boldsymbol{\alpha}^{(\ell)} \rrbracket_i, \llbracket C^{(\ell)} \rrbracket_i, \llbracket K^{(\ell)} \rrbracket_i \leftarrow \operatorname{PRG}(\mathsf{salt}, \mathsf{seed}_i^{(\ell)}), \quad \mathsf{state}_i^{(\ell)} \leftarrow \mathsf{seed}_i^{(\ell)}$ 4: $(i_1, \ldots, i_D) \leftarrow i$ Note: Representation of i in $[N]^D$ 5: for $k \in [D]$ do $[\![A^{(\ell)}]\!]_{(k,i_k)} \leftarrow [\![A^{(\ell)}]\!]_{(k,i_k)} + [\![A^{(\ell)}]\!]_i, \quad [\![\alpha^{(\ell)}]\!]_{(k,i_k)} \leftarrow [\![\alpha^{(\ell)}]\!]_{(k,i_k)} + [\![\alpha^{(\ell)}]\!]_i,$ 6: $[\![C^{(\ell)}]\!]_{(k,i_k)} \leftarrow [\![C^{(\ell)}]\!]_{(k,i_k)} + [\![C^{(\ell)}]\!]_i, \quad [\![K^{(\ell)}]\!]_{(k,i_k)} \leftarrow [\![K^{(\ell)}]\!]_{(k,i_k)} + [\![K^{(\ell)}]\!]_i$ 7. $[\![A^{(\ell)}]\!]_{N^D}, \leftarrow \mathrm{PRG}(\mathsf{salt}, \mathsf{seed}_{N^D}^{(\ell)}), \quad [\![\boldsymbol{\alpha}^{(\ell)}]\!]_{N^D} \leftarrow \boldsymbol{\alpha} - \sum\nolimits_{i \neq N^D} [\![\boldsymbol{\alpha}^{(\ell)}]\!]_i$ 8: $\llbracket K^{(\ell)} \rrbracket_{N^D} \leftarrow K - \sum\nolimits_{i \neq N^D} \llbracket K^{(\ell)} \rrbracket_i, \quad \llbracket C^{(\ell)} \rrbracket_{N^D} \leftarrow A^{(\ell)} \cdot K - \sum\nolimits_{i \neq N^D} \llbracket C^{(\ell)} \rrbracket_i$ 9: for $k \in [D]$ do $[\![A^{(\ell)}]\!]_{(k,N)} \leftarrow [\![A^{(\ell)}]\!]_{(k,N)} + [\![A^{(\ell)}]\!]_{N^D}, \quad [\![\boldsymbol{\alpha}^{(\ell)}]\!]_{(k,N)} \leftarrow [\![\boldsymbol{\alpha}^{(\ell)}]\!]_{(k,N)} + [\![\boldsymbol{\alpha}^{(\ell)}]\!]_{N^D},$ 10: $[\![C^{(\ell)}]\!]_{(k,N)} \leftarrow [\![C^{(\ell)}]\!]_{(k,N)} + [\![C^{(\ell)}]\!]_{N^D}, \quad [\![K^{(\ell)}]\!]_{(k,N)} \leftarrow [\![K^{(\ell)}]\!]_{(k,N)} + [\![K^{(\ell)}]\!]_{N^D}$ 11: $\mathsf{aux}^{(\ell)} \leftarrow (\llbracket \pmb{\alpha}^{(\ell)} \rrbracket_{N^D}, \llbracket K^{(\ell)} \rrbracket_{N^D}, \llbracket C^{(\ell)} \rrbracket_{N^D}), \quad \mathsf{state}_{N^D}^{(\ell)} \leftarrow (\mathsf{seed}_{N^D}^{(\ell)}, \mathsf{aux}^{(\ell)})$ $\operatorname{com}_{i}^{(\ell)} \leftarrow \operatorname{Hash}_{0}(\operatorname{salt}, \ell, i, \operatorname{state}_{i}^{(\ell)}), \text{ for all } i \in [N^{D}]$ 13: $\operatorname{com}^{(\ell)} \leftarrow \operatorname{Hash}_{(1,0)}(\operatorname{salt}, \ell, (\operatorname{com}_i^{(\ell)})_{i \in [N^D]})$ 14: Phase 2: First challenges $h_1 \leftarrow \operatorname{Hash}_{(1,1)}(\mathsf{msg}, \mathsf{salt}, (\mathsf{com}^{(\ell)})_{\ell \in [\tau]})$ 15: $R^{(1)}, \ldots, R^{(\tau)} \leftarrow \operatorname{PRG}(h_1)$ 16: Phase 3: Simulation of the MPC protocols for $\ell \in [\tau]$ do for $k \in [D]$ do for $j \in [N]$ do Compute $\llbracket \boldsymbol{M}_{\boldsymbol{\alpha}}^{L,(\ell)} \rrbracket_{(k,j)}, \llbracket \boldsymbol{M}_{\boldsymbol{\alpha}}^{R,(\ell)} \rrbracket_{(k,j)}$ from $\llbracket \boldsymbol{\alpha}^{(\ell)} \rrbracket_{(k,j)}$ 17: $\llbracket S^{(\ell)} \rrbracket_{(k,j)} \leftarrow R^{(\ell)} \cdot \llbracket \boldsymbol{M}_{\boldsymbol{\alpha}}^{R,(\ell)} \rrbracket_{(k,j)} + \llbracket A^{(\ell)} \rrbracket_{(k,j)}$ 18: $S_k^{(\ell)} \leftarrow \sum\nolimits_{j \in [N]} [\![S^{(\ell)}]\!]_{(k,j)}$ 19: $[\![V^{(\ell)}]\!]_{(k,j)} \leftarrow S_k^{(\ell)} \cdot [\![K^{(\ell)}]\!]_{(k,j)} - R^{(\ell)} \cdot [\![M^{L,(\ell)}_{\alpha}]\!]_{(k,j)} - [\![C^{(\ell)}]\!]_{(k,j)}, \text{ for all } j \in [N]$ 20: $H_k^{(\ell)} \leftarrow \operatorname{Hash}_{(2,0)} \left(\mathsf{salt}, \ell, \left([\![S^{(\ell)}]\!]_{(k,j)}, [\![V^{(\ell)}]\!]_{(k,j)} \right)_{i \in [N]} \right)$ 21: Phase 4: Second challenges $h_2 \leftarrow \operatorname{Hash}_{(2,1)}\left(\mathsf{msg},\mathsf{salt},h_1,\left(H_1^{(\ell)},\ldots,H_D^\ell\right)_{\ell \in [\tau]}\right)$ 22: 23: $i^{*,(1)}, \ldots, i^{*,(\tau)} \leftarrow \text{PRG}(h_2)$, where each $i^{*,(\ell)} \in [N^D]$ Phase 5: Assembling the signature σ 24: $\sigma \leftarrow \left(\mathsf{salt}, h_1, h_2, \left(\left(\mathsf{state}_i^{(\ell)}\right)_{i \neq i^*, (\ell)}, \mathsf{com}_{i^*, (\ell)}^{(\ell)}, \left[\!\left[S^{(\ell)}\right]\!\right]_{i^*, (\ell)}\!\right)_{\ell \in [r_*]}\right)$ return σ

Figure 7: Signing algorithm of MIRITH-HYPERCUBE.

 $VerifHypercube(M, msg, \sigma)$ 1: $R^{[1]}, \dots, R^{(\tau)} \leftarrow \text{PRG}(h_1), \quad i^{*,[1]}, \dots, i^{*,(\tau)} \leftarrow \text{PRG}(h_2)$ for all $\ell \in [\tau]$ do $\operatorname{com}_{i}^{(\ell)} \leftarrow \operatorname{Hash}_{0}\left(\mathsf{salt}, \ell, i, \mathsf{state}_{i}^{(\ell)}\right), \text{ for all } i \in [N^{D}] \setminus \{i^{*,(\ell)}\}$ 2: $\mathsf{com}^{(\ell)} \leftarrow \mathrm{Hash}_{(1,0)}\big(\mathsf{salt}, \ell, (\mathsf{com}_i^{(\ell)})_{i \in [N^D]}\big)$ 3 : $(i_1^{*,(\ell)}, \dots, i_D^{*,(\ell)}) \leftarrow i^{*,(\ell)}$ 4: for $k \in [D]$ do Compute $[\![S^{(\ell)}]\!]_{(k,j)}$ as in SignHypercube(), for all $j \in [N] \setminus \{i_k^{*,(\ell)}\}$ 5: $S_k^{(\ell)} \leftarrow \sum\nolimits_{j \in [N]} \llbracket S^{(\ell)} \rrbracket_{(k,j)}$ 6: Compute $[V^{(\ell)}]_{(k,j)}$ as in SignHypercube(), for all $j \in [N] \setminus \{i_k^{*,(\ell)}\}$ 7: $[\![V^{(\ell)}]\!]_{(k,i_k^{*,(\ell)})} \leftarrow -\sum\nolimits_{j \neq i_k^{*,(\ell)}} [\![V^{(\ell)}]\!]_{(k,j)}$ 8: $H_k^{(\ell)} \leftarrow \operatorname{Hash}_{(2,0)}\left(\mathsf{salt}, \ell, \left(\llbracket S^{(\ell)} \rrbracket_{(k,j)}, \llbracket V^{(\ell)} \rrbracket_{(k,j)}\right)_{i \in [N]}\right)$ 9: $h'_1 \leftarrow \operatorname{Hash}_{(1,1)}(\mathsf{msg},\mathsf{salt},(\mathsf{com}^{(\ell)})_{\ell \in [\tau]})$ 10: $h'_2 \leftarrow \operatorname{Hash}_{(2,1)}(\mathsf{msg},\mathsf{salt},h'_1,(H_1^{(\ell)},\ldots,H_D^{\ell})_{\ell \in [\tau]})$ 11: Output **accept** if $h'_1 = h_1$ and $h'_2 = h_2$, otherwise output **reject** 12:

Figure 8: Verification algorithm of MIRITH-HYPERCUBE.

4 Security Evaluation

The security of MIRITH formally relies on the security of the ZKPoK from Section 3.1.2, the Fiat–Shamir transform, and the hardness of solving random instances of the MinRank problem.

The Fiat–Shamir transform is known to be secure in the Random Oracle Model (ROM) and has been the target of extensive security analysis since its invention.

The completeness, soundness, and zero-knowledge properties of the sigma protocol as well as the fact that the resulting signature scheme from Section 3 is existentially unforgeable under chosen messages attacks (EU-CMA) are proved in the ROM in Section 9.

The hardness of the MinRank problem is well established in cryptography, since, due to its many applications in cryptanalysis, algorithms to solve the problem have been extensively studied. In the following, we give an overview of the best-known attacks and their complexities.

4.1 Known attacks to MinRank

There are two types of attacks on the MinRank problem: Combinatorial and algebraic. On the combinatorial side, we have the well-known kernel-search attack. In contrast, on the algebraic side, several approaches model a MinRank instance as a system of multivariate polynomial equations [FEDS13, KS99, VBC⁺19]. Still, they remain less efficient versions of the support-minors modeling [BB22, GD22]. In the following, we detail the most efficient approaches of both categories, as well as a recent hybrid strategy.

In this section, $\omega \in [2,3]$ denotes the constant of matrix multiplication, i.e., two $n \times n$ matrices over a field can be multiplied in $\mathcal{O}(n^{\omega})$ field operations.

4.1.1 Enumeration

Enumerate coefficients The most trivial way to solve the MinRank problem is by trying all possible solution vectors in \mathbb{F}_q^k . For each vector, we compute the corresponding linear combination with the input matrices M and check if it has rank at most r. The complexity of this attack is $\mathcal{O}(q^k \cdot \max(m, n)^{\omega})$.

Enumerate entries of *E* Another way is to enumerate some entries of the hidden matrix *E*. This attack is particularly effective when (m-r)(n-r) < k, and it is known as the big-*k* attack³.

Finally, we can enumerate the entries of the matrix K, which is equivalent to searching elements in the kernel of the hidden low-rank matrix. The following section explains this attack in a more general fashion.

4.1.2 Kernel-search

The kernel-search algorithm was introduced by Goubin and Courtois [GC00]. It consists of guessing $\lceil k/m \rceil$ linearly independent vectors in the kernel of the unknown rank r matrix E. The expected complexity of this algorithm, in terms of multiplications in \mathbb{F}_q , is

$$\mathcal{O}\left(q^{r\cdot\lceil k/m\rceil}\cdot k^{\omega}\right)$$

4.1.3 Support-minors modeling

The support-minors (SM) modeling is an algebraic method to solve the MinRank problem, which was introduced by Bardet et al. [BBC⁺20]. It models an instance of the MinRank problem as a bilinear system of equations that are then solved by an XL-like algorithm.

For q > 2, the complexity of solving the SM equations is computed as

$$\min\left\{3 \cdot k(r+1) \cdot A(b,n')^2, 7 \cdot A(b,n')^{\omega} \mid \frac{1 \le b \le r+1,}{r+b \le n' \le n,} \atop A(b,n')^{-1 \le B(b,n')}\right\},\$$

where

$$A(b,n') := \binom{n'}{r} \binom{k+b-1}{b},$$

$$B(b,n') := \sum_{j=1}^{b} \sum_{i=1}^{j} \left\{ (-1)^{i+1} \binom{n'}{r+i} \binom{m+i-1}{i} \binom{k}{j-i} \right\}.$$

In Bardet et al. [BBB⁺22], the authors showed a new guess-and-solve (or hybrid) approach for the MinRank, which is more efficient than directly solving a given instance.

³Originally called the big-m attack since m the variable used to denote the number of matrices in the MinRank problem [Cou01].

4.1.4 Hybrid approaches

We consider two hybridization approaches to estimate the number of multiplications in \mathbb{F}_q required to solve a given MinRank instance. The first approach guesses l_v coefficients of the solution vector $\boldsymbol{\alpha}$. The second approach, introduced by Bardet et al. [BBB⁺22], guesses *a* vectors (with a specific structure) in the right kernel of the secret *E*. When both approaches are combined, one derives MinRank instances $\tilde{\boldsymbol{M}}$ with parameters $(q, m, n - a, k - am - l_v, r)$. Hence the complexity of this hybrid approach is given by

$$q^{a \cdot r + l_v} \Big(\mathsf{MR}_{-}\mathsf{Complexity}(q, m, n - a, k - am - l_v, r) \\ + \Big(\min\{k, am\} \Big)^{\omega} \Big), \tag{4}$$

where $MR_Complexity(\cdot)$ returns the complexity to solve a random instance of the MinRank problem defined by the input parameters.

Note that, while improving on state of the art, the impact on parameter selection for MIRITH by this attack has been moderate with decreasing the security level of our conservative instantiations by about 4 (category I) to 14 bits (category V).

4.1.5 Quantum attacks

There are at least two ways to do a Grover search in order to solve the MinRank problem. The first consists of searching the solution vector $\boldsymbol{\alpha} \in \mathbb{F}_q^k$. The other consists of searching some columns of the secret matrix K from Eq. (2).

For our proposed parameters (Table 1), we found that the quantum search over the entries of K is more efficient than the search over the coordinates of α . Hence, we focus on describing this attack.

Proposition 1. Let $M = (M_0, \ldots, M_k)$ be a MinRank instance, where $M_\ell = [\mu_{(i,j)}^{(\ell)}]_{i,j=1}^{m,n} \in \mathbb{F}_q^{m \times n}$ with solution α . Let $K = [\kappa_{(i,j)}]_{i,j=1}^{r,n-r} \in \mathbb{F}_q^{r \times (n-r)}$ be as in Eq. (2) and $1 \le t \le n-r$ be an integer. For $\mathbf{x}_t = (x_{(1,1)}, x_{(1,2)}, \ldots, x_{(r,t)}) \in \mathbb{F}_q^{rt}$ define

$$B(\mathbf{x}_t) = \begin{pmatrix} B_1(\mathbf{x}_t) \\ \vdots \\ B_t(\mathbf{x}_t) \end{pmatrix}^\top \in \mathbb{F}_q^{(k+1) \times (mt)},$$

where

$$B_{j}(\mathbf{x}_{t}) = -\begin{pmatrix} \mu_{(1,j)}^{(1)} & \mu_{(1,j)}^{(k)} & \mu_{(1,j)}^{(0)} \\ \vdots & \dots & \vdots & \vdots \\ \mu_{(m,j)}^{(1)} & \mu_{(m,j)}^{(k)} & \mu_{(m,j)}^{(0)} \end{pmatrix} + \\ + \begin{pmatrix} \sum_{i=1}^{r} \mu_{(1,n-r+i)}^{(1)} x_{(i,j)} & \sum_{i=1}^{r} \mu_{(1,n-r+i)}^{(k)} x_{(i,j)} & \sum_{i=1}^{r} \mu_{(1,n-r+i)}^{(0)} x_{(i,j)} \\ \vdots & \dots & \vdots & \vdots \\ \sum_{i=1}^{r} \mu_{(m,n-r+i)}^{(1)} x_{(i,j)} & \sum_{i=1}^{r} \mu_{(m,n-r+i)}^{(k)} x_{(i,j)} & \sum_{i=1}^{r} \mu_{(m,n-r+i)}^{(0)} x_{(i,j)} \end{pmatrix}.$$

Then it holds that

$$(\alpha_1,\ldots,\alpha_k,1)\cdot B(\boldsymbol{\kappa}_t)=\mathbf{0}.$$

Note that Proposition 1 implies that the matrix $B(\kappa_t)$ is not of full rank. For the attack, we ensure by choice of t that the only \mathbf{x}_t that leads to a $B(\mathbf{x}_t)$ with a rank defect is κ_t . Therefore, we fix $\lceil k/m \rceil \leq t \leq n-r$ such that $rt \leq \binom{mt}{k+1}$. Hence $k+1 \leq mt$, and $\kappa_t \in \mathbb{F}_q^{rt}$ is expected to be the only vector such that $B(\kappa_t)$ is not full-rank. Indeed, the total number of different matrices $B(\gamma)$ with $\gamma \in \mathbb{F}_q^{rt}$ is at most q^{rt} , each of them has $\binom{mt}{k+1}$ maximal minors, and any of these minors is zero with probability close to 1/q. Finally, note that once κ_t is known, the MinRank solution $(\alpha_1, \ldots, \alpha_k)$ can be efficiently recovered by classical Gaussian elimination using the relation from Proposition 1.

Define $\Psi : (\mathbb{F}_2^{\log q})^{rt} \to \mathbb{F}_q^{rt}$ such that $\Psi(\mathbf{v}_1, \dots, \mathbf{v}_{rt}) := (\gamma_1, \dots, \gamma_{rt})$, where \mathbf{v}_i is the binary representation of γ_i . We use Grover's algorithm to find the vector $\boldsymbol{\kappa}_t \in \mathbb{F}_q^{rt}$ representing the first t columns of the secret matrix K. This algorithm uses a quantum oracle $\mathcal{F} : \mathbb{F}_2^{rt \log q} \to \mathbb{F}_2$ such that

$$\mathcal{F}(\mathbf{v}) = \begin{cases} 1 & \text{if } \Psi(\mathbf{v}) = \kappa_t \\ 0 & \text{otherwise.} \end{cases}$$

Our quantum oracle \mathcal{F} on input $\mathbf{v} \in \mathbb{F}_2^{rt \log q}$ splits into three parts:

- 1. Compute the matrix $B(\boldsymbol{\gamma})$, where $\boldsymbol{\gamma} = \Psi(\mathbf{v})$.
- 2. Compute, by Gaussian elimination, the row-reduced form of $B(\gamma)$.
- 3. Negate every qubit representing the last column of $B(\boldsymbol{\gamma})$, and then output the multiplication of such qubits.

Notice $\mathcal{F}(\mathbf{v}) = 1$ if and only if at the end of step 2, the last row of $B(\boldsymbol{\gamma})$ is the zero vector.

Grover's algorithm states that the vector κ_t can be found with high probability after $\mathcal{O}(2^{(rt\log q)/2})$ calls to the quantum oracle \mathcal{F} . The complexity of computing \mathcal{F} is dominated by the Gaussian elimination step, which can be performed in $\mathcal{O}((k+1)^2 \cdot mt)$ operations in \mathbb{F}_q . Therefore, we estimate our quantum complexity to be

$$\mathcal{O}(2^{(rt\log q)/2} \cdot (k+1)^2 mt)$$

operations over \mathbb{F}_q .

Limited circuit depth For the quantum security definition of categories I, III and V, the maximum depth of the used quantum circuits is limited to 2^{maxdepth} with $\text{maxdepth} \leq 96$. A parameter set is said to match the quantum security definition for a category if an attack requires at least $2^{b-\text{maxdepth}}$ quantum gates for b = 170, 233, 296 for category I, III and V, respectively.

We lower bound the depth of the described quantum circuit by

$$D = 2^{(rt\log q)/2}k^2,$$

which corresponds to the sequential repetition of the Grover iterations, where we lower bound the depth of the oracle with k^2 .

In the case of $D > 2^{\text{maxdepth}}$, the most efficient strategy to restrict the depth of the quantum circuit is to partition the search space in P equally sized sets [Zal99]. Then the search has to be reapplied for each partition, which comes at a depth of

$$D_P = \frac{D}{\sqrt{P}}.$$

and leads to $D_P = 2^{\text{maxdepth}}$ for a choice of $P = (D/2^{\text{maxdepth}})^2$.

The total number of quantum gates necessary to launch the depth-limited attack becomes

$$T = \mathcal{O}(P \cdot D_P \cdot mt \log^2 q), \tag{5}$$

where we count $\log^2 q$ gates per field multiplication.

4.2 Known attacks to the protocol

It is possible to forge a signature without solving the underlying instance of the MinRank problem. For signature schemes built by applying the Fiat-Shamir transformation on a five-pass identification scheme, the best-known attack of this type was introduced by Kales and Zaverucha [KZ20]. It is known as the KZ attack.

In our case, the complexity of the KZ attack is measured as the expected number of calls to $Hash_1$ and $Hash_2$. And, it is given by

$$C(\tau, q, s, N) = \min_{0 \le k \le \tau} \left\{ \frac{1}{\sum_{i=k}^{\tau} \left(\frac{1}{q^s}\right)^i \left(1 - \frac{1}{q^s}\right)^{\tau-i} {\tau \choose i}} + N^{\tau-k} \right\}.$$
 (6)

5 Parameters

In Table 1 and Table 2, we propose parameter sets for MIRITH and MIRITH-HYPERCUBE, respectively, for the different security levels I, III, and V defined by NIST, which correspond to 143, 207, and 272 bits of classical security, respectively [NIS].

The MinRank problem parameters (q, m, n, k, r) are chosen with respect to the best-known attacks sketched in Section 4.1. In Table 3, we state the estimated quantum security margin for each parameter set. That is the quotient of the gates necessary to launch a quantum attack according to Eq. (5) and the defined security threshold of $2^{b-\max depth}$ for b = 170, 233, 296 for category I, III and V respectively. Note that even though the set IIIa lies slightly below the quantum security threshold, these 3 bits are easily compensated by the numerous disregarded overhead factors that a practical quantum implementation would entail.

The number of rows of the challenge matrices R, the number of parties, N for MIRITH and N^D for MIRITH-HYPERCUBE, in the underlying MPC protocol and the number τ of repetitions of the identification protocol are computed such that the forgery cost in Eq. (6) is at least 2^{λ} . We provide two classes of parameter sets, namely: a and b sets. The Xa sets minimize the signature size, while precisely matching the NIST security levels definitions. On the other hand, with the Xb sets, we provide a more conservative choice of parameters which include some security margin to account for potential future attack improvements.

Further, for each parameter set, we provide a "Fast" and a "Short" (a "Shorter" and a "Shortest", in addition, for MIRITH-HYPERCUBE) variant leveraging a trade-off between signing/verification time and signature size that originates from the use of the MPC protocol. In fact,

Set	Variant	λ	q	m	n	k	r	s	N	τ	Bit security
Ia	fast short	128	16	15	15	78	6	$\frac{5}{9}$	$\frac{16}{256}$	39 19	144
Ib	fast short	128	16	16	16	142	4	$\frac{5}{9}$	$\begin{array}{c} 16 \\ 256 \end{array}$	39 19	159
IIIa	fast short	192	16	19	19	109	8	7 9	$\begin{array}{c} 16 \\ 256 \end{array}$	$55 \\ 29$	207
IIIb	fast short	192	16	19	19	167	6	79	$\begin{array}{c} 16 \\ 256 \end{array}$	$55 \\ 29$	232
Va	fast short	256	16	21	21	189	7	7 10	$\begin{array}{c} 16 \\ 256 \end{array}$	$\frac{74}{38}$	273
Vb	fast short	256	16	22	22	254	6	7 10	$\frac{16}{256}$	$\frac{74}{38}$	301

lowering the number of MPC parties allows one to reduce the computation time, but to maintain the same forgery cost, the number of repetitions has to be increased, which in turn results in larger signature sizes.

Table 1: Parameters of MIRITH.

Set	Variant	λ	q	m	n	k	r	s	N	D	τ	Bit security
Ia	fast short shorter shortest	128	16	15	15	78	6	5 9 12 12	2 2 2 2	4 8 12 16	39 19 13 10	144
Ib	fast short shorter shortest	128	16	16	16	142	4	5 9 12 12	2 2 2 2	4 8 12 16	39 19 13 10	159
IIIa	fast short shorter shortest	192	16	19	19	109	8	7 9 13 13	2 2 2 2	4 8 12 16	55 29 19 15	207
IIIb	fast short shorter shortest	192	16	19	19	167	6	7 9 13 13	2 2 2 2	4 8 12 16	55 29 19 15	232
Va	fast short shorter shortest	256	16	21	21	189	7	$10 \\ 10 \\ 14 \\ 14$	2 2 2 2	4 8 12 16	71 38 26 20	273
Vb	fast short shorter shortest	256	16	22	22	254	6	$10 \\ 10 \\ 14 \\ 14$	2 2 2 2	$ \begin{array}{c} 4 \\ 8 \\ 12 \\ 16 \end{array} $	71 38 26 20	301

Table 2: Parameters of MIRITH-HYPERCUBE.

Set	t	$\log(\text{gates})$
Ia	6	10
Ib	10	30
IIIa	6	-3
IIIb	9	24
Va	10	24
Vb	12	34

Table 3: Quantum security margin of MIRITH and MIRITH-HYPERCUBE

6 Implementation Notes

Field elements We proposed parameters for our scheme over the field \mathbb{F}_{16} , in which every element is represented by a binary polynomial of degree 3, e.g., $a_3x^3 + a_2x^2 + a_1x + a_0$.

In our implementation, the multiplication of two elements in \mathbb{F}_{16} is implemented as a polynomial multiplication modulus the polynomial $x^4 + x + 1$. An element $a_3x^3 + a_2x^2 + a_1x + a_0 \in \mathbb{F}_{16}$ is stored $a_3 \cdot 2^3 + a_2 \cdot 2^2 + a_1 \cdot 2 + a_0$

Matrix representation A matrix M of dimensions $n_r \times n_c$ is represented as a byte-array \mathbf{A}_M of length $\lfloor (n_r+1)/2 \rfloor n_c$. The array \mathbf{A}_M is the concatenation of n_c byte-arrays $\mathbf{A}_{M,0}, \mathbf{A}_{M,1}, \ldots, \mathbf{A}_{M,n_c-1}$, where the array $\mathbf{A}_{M,j}$ stores the *j*-th column of M. Let $\epsilon_{i,j}$ be the (i,j) entry of M. Then, for $0 \leq j < n_c$ and $0 \leq i < \lfloor (n_r-2)/2 \rfloor, \mathbf{A}_{M,j}[i] = \epsilon_{j,2i} + 16 \cdot \epsilon_{j,2i+1}$. If n_r is odd, $\mathbf{A}_{M,j}[(n_r+1)/2-1] = \epsilon_{n_r-1,j}$.

Matrix packing We pack a matrix $M \in \mathbb{F}_{16}^{n_r \times n_c}$ into a string of byte \mathbb{P}_M in two different ways, which differ on how we use the first byte $\mathbb{P}_M[0]$ of the byte string \mathbb{P}_M to store elements of M.

We use a binary parameter, called b_o , to indicate how the first byte of P_M should be used to pack entries of M. When $b_o = 0$, we use $P_M[0]$ to pack to entries of M. Otherwise, when $b_o = 1$, we pack into $P_M[0]$ only one entry of M.

The reason for the matrix packing strategy when $b_o = 1$ is that, while packing a signature, we have to pack several matrices, and some of them might have an odd number of entries. Hence, we cannot fill a whole byte array P_M only with the entries of M; at least one byte will be half-filled. Such a half-filled byte of P_M must (otherwise, we could trivially forge a signature) be then used to pack one entry of another a matrix during the signature packing process.

Now we precisely describe how the matrix packing works in our reference implementation. In what follows, we denote by e_i the *i*-th entry of M in a column-major order:

• If n_r is even:

- If $b_o = 0$, we set

$$\mathbf{P}_{M}[i] = e_{2i} + 16 \cdot e_{2i+1}, \text{ for } i = 0, \dots, (n_{r}n_{c} - 2)/2.$$

- If $b_o = 1$, we set $P_M[0] = 16 \cdot e_{n_r n_c - 1}$

 $P_M[i] = e_{2i-1} + 16 \cdot e_{2i}$, for $i = 1, \dots, (n_r n_c - 3)/2$,

and $P_M[(n_rn_c - 3)/2 + 1] = e_{n_rn_c-2}$.

• If n_r odd: Let R_{n_r} denote the last row of M, $\gamma_0, \ldots, \gamma_{n_c-1}$ the entries of R_{n_r} , and let $M_{sub} \in \mathbb{F}_{16}^{(n_r-1) \times n_c}$ be the matrix that results from removing R_{n_r} from M.

- If $b_o = 0$, we first pack M_{sub} as in the n_r -even case. Then, we set

$$\mathsf{P}_M\left[\frac{(n_r-1)n_c}{2}+i\right] = \gamma_{2i} + 16 \cdot \gamma_{2i+1}, \text{ for } i = 0, \dots, \lfloor (n_c-2)/2 \rfloor$$

If n_c is odd, then we set $P_M[(n_r n_c - 1)/2] = \gamma_{n_c-1}$.

- If $b_o = 1$, we first set $P_M[0] = 16 \cdot \gamma_{n_c-1}$. Then, we pack M_{sub} as in the n_r -even case with $b_o = 0$ starting from $P_M[1]$. Finally, we set

$$\mathsf{P}_M\left[\frac{(n_r-1)n_c}{2} + 1 + i\right] = \gamma_{2i} + 16 \cdot \gamma_{2i+1}, \text{ for } i = 0, \dots, \lfloor (n_c - 3)/2 \rfloor.$$

If n_c is even, then we set $P_M[(n_r n_c)/2 - 1] = \gamma_{n_c-2}$.

Implementations We provide three implementations of MIRITH, one reference implementation and two optimized implementations. The first optimized implementation is for Inter(R) processors and optimizes the matrix arithmetic by using AVX2 instructions. The second optimized implementation is for ARM processors and optimizes the matrix arithmetic by using NEON instructions.

7 Performance Analysis

7.1 Public key and secret key sizes

The public key and secret key sizes for the different parameter sets in Section 5 are the same for both MIRITH (Table 4) and MIRITH-HYPERCUBE (Table 8). As given in Section 3.2, the public key size is $\lambda + (m(n-r) - k) \log q$ bits and the secret key is a λ -bit seed, from which the secret MinRank solution is derived.

7.2 MiRitH

7.2.1 Signature sizes

From Section 3.3, it follows that the maximum size (in bits) for a MIRITH signature is equal to

$$\underbrace{6\lambda}_{\mathsf{salt},h_1,h_2} + \tau \left(\underbrace{(k + r(n-r) + s(n-r) + sr) \cdot \log q}_{\left[\!\left[\pmb{\alpha}^{(\ell)}\right]\!\right]_N, \left[\!\left[K^{(\ell)}\right]\!\right]_N, \left[\!\left[C^{(\ell)}\right]\!\right]_N, \left[\!\left[S^{(\ell)}\right]\!\right]_{i^*, (\ell)}} + \underbrace{\lambda \cdot \log_2 N}_{(\mathsf{seed}_i^{(\ell)})_{i \neq i^*}} + \underbrace{2\lambda}_{\mathsf{com}_{i^*, (\ell)}} \right),$$

while the average size (in bits) is equal to

$$\underbrace{6\lambda}_{\mathsf{salt},h_1,h_2} + \tau \left(\underbrace{\frac{N-1}{N}}_{\Pr[i^*,(\ell) \neq N]} \cdot \underbrace{\left(k + r(n-r) + s(n-r)\right)\log q}_{\left[\!\left[\boldsymbol{\alpha}^{(\ell)}\right]\!\right]_N, \left[\!\left[K^{(\ell)}\right]\!\right]_N, \left[\!\left[C^{(\ell)}\right]\!\right]_N} + \underbrace{sr\log q}_{\left[\!\left[S^{(\ell)}\right]\!\right]_i *} \cdot \underbrace{\frac{\lambda \cdot \log N}{\left(\mathsf{seed}_i^{(\ell)}\right)_{i \neq i^*}, (\ell)}} + \underbrace{2\lambda}_{\mathsf{com}_{i^*,(\ell)}} \right).$$

where s is the MPC protocol Π_s parameter, N the number of parties in one MPC protocol and τ the number of parallel repetitions, respectively. The values N and τ are set to achieve a soundness error smaller than $2^{-\lambda}$.

Table 4 shows the sizes of the public key, secret key, maximum signature and average signature for all the proposed parameter sets for MIRITH.

Set	Variant	Public key	Secret key	Signature (max)	Signature (avg)
Ia	fast short	129	16	7,877 5,673	$7,661 \\ 5,665$
Ib	fast short	144	16	$9,105 \\ 6,309$	$8,800 \\ 6,298$
IIIa	fast short	205	24	$17,139 \\ 12,440$	$16,\!668$ $12,\!423$
IIIb	fast short	205	24	$18,459 \\ 13,136$	$17,882 \\ 13,115$
Va	fast short	253	32	$30,458 \\ 21,795$	$29,568 \\ 21,763$
Vb	fast short	274	32	$33,048 \\ 23,182$	$31,\!980$ $23,\!144$

Table 4: Public key, secret key, and signature sizes of MIRITH in bytes.

7.2.2 Runtime

We provide an optimized implementation using AVX2 for MIRITH. The key generation, signature generation, and verification benchmarks were performed on an 11th Gen Intel(R) Core(TM) i7-11850H @ 2.50GHz (Turbo Boost disabled). The reported timings are the medians and are shown in Table 5, where they are given in CPU cycles. This implementation uses the AVX2 optimized SHA3/SHAKE implementation from the *eXtended Keccak Code Package* (XKCP) ⁴.

⁴https://github.com/XKCP/XKCP

Set	Variant	Key generation	Signing	Verification
Ia	fast short	108,903	8,703,311 76,549,995	$7,311,069 \\76,874,731$
Ib	fast short	199,556	$\begin{array}{c} 8,015,345\\ 65,630,977\end{array}$	7,558,761 65,551,641
IIIa	fast short	246,740	22,485,807 192,858,411	$18,\!431,\!919\\175,\!520,\!472$
IIIb	fast short	373,836	24,538,474 242,531,804	22,470,437 204,853,275
Va	fast short	508,607	36,361,915 308,565,196	36,665,342 310,604,452
Vb	fast short	693,941	38,659,453 327,068,513	38,122,610 330,632,038

Table 5: Median time (in clock cycles) for the AVX2 implementation of MIRITH on an 11th Gen Intel(R) Core(TM) i7-11850H @ 2.50GHz (Turbo Boost disabled).

Additionally, we provide an optimized implementation for the ARM architecture using the NEON instruction set. All benchmarks were performed on an Apple M1 Max CPU, which are listed in Table 6. This implementation also leverages the XKCP for its SHA3/SHAKE implementation.

Set	Variant	Key generation	Signing	Verification
Ia	fast short	84,260	$\begin{array}{c} 4,802,\!648 \\ 42,\!901,\!724 \end{array}$	$\begin{array}{c} 4,463,499\\ 42,711,128\end{array}$
Ib	fast short	159,018	6,380,382 51,492,738	5,933,225 51,801,192
IIIa	fast short	207,518	11,234,568 94,497,277	$10,\!390,\!944 \\94,\!227,\!698$
IIIb	fast short	315,849	$13,\!284,\!941$ $112,\!205,\!153$	12,349,311 112,013,158
Va	fast short	432,562	23,891,660 196,659,024	22,245,016 194,568,129
Vb	fast short	599,738	$28,311,245 \\241,600,195$	26,328,290 240,977,263

Table 6: Median time (in clock cycles) for the NEON implementation of MIRITH on an Apple M1 Max.

7.2.3 Memory

We performed an analysis of the maximum memory usage of MIRITH by employing the massif tool of valgrind. The results are reported in Table 7. As valgrind is only available on Linux, we could

not perform the memory analysis on our Apple machine, but we expect no significant difference in stack usage.

Set	Variant	Key generation	Signing	Verification
Ia	fast short	11,392	$ \begin{array}{r} 32,632 \\ 1,018,552 \end{array} $	31,768 101,144
Ib	fast short	20,096	$161,816 \\ 1,193,304$	44,024 127,416
IIIa	fast short	23,184	286,376 2,196,552	64,616 157,032
IIIb	fast short	34,120	$318,600 \\ 2,386,248$	78,952 180,712
Va	fast short	46,488	516,984 3,815,864	116,424 230,680
Vb	fast short	64,216	570,296 4,116,984	140,408 265,272

Table 7: Memory usage (in bytes) for the AVX2 implementation of MIRITH. Benchmarks were done a 12th Gen Intel(R) Core(TM) i5-1240P.

7.3 MiRitH-Hypercube

7.3.1 Signature sizes

From Section 3.4, it follows that the maximum size (in bits) for a MIRITH-HYPERCUBE signature is equal to

$$\underbrace{6\lambda}_{\mathsf{salt},h_1,h_2} + \tau \left(\underbrace{(k + r(n-r) + s(n-r) + sr) \cdot \log q}_{\left[\!\left[\pmb{\alpha}^{(\ell)}\right]\!\right]_N, \left[\!\left[K^{(\ell)}\right]\!\right]_N, \left[\!\left[C^{(\ell)}\right]\!\right]_N, \left[\!\left[S^{(\ell)}\right]\!\right]_{i^*, (\ell)}} + \underbrace{\lambda \cdot D \cdot \log_2 N}_{(\mathsf{seed}_i^{(\ell)})_{i \neq i^*}} + \underbrace{2\lambda}_{\mathsf{com}_{i^*, (\ell)}} \right),$$

while the average size (in bits) is equal to

$$\underbrace{\frac{6\lambda}{\mathsf{salt},h_1,h_2}}_{\mathsf{salt},h_1,h_2} + \tau \left(\underbrace{\frac{N-1}{N}}_{\Pr[i^*,(\ell) \neq N]} \cdot \underbrace{(k+r(n-r)+s(n-r))\log q}_{[\![\alpha^{(\ell)}]\!]_N, [\![K^{(\ell)}]\!]_N, [\![C^{(\ell)}]\!]_N} + \underbrace{sr\log q}_{[\![S^{(\ell)}]\!]_{i^*}} \cdot \underbrace{\lambda \cdot D \cdot \log N}_{(\mathsf{seed}_i^{(\ell)})_{i \neq i^*,(\ell)}} + \underbrace{2\lambda}_{\mathsf{com}_{i^*,(\ell)}} \right),$$

where s is the MPC protocol Π_s parameter, N the number of parties in one MPC protocol, D the hypercube dimension and τ the number of parallel repetitions, respectively. The values N and τ are set to achieve a soundness error smaller than $2^{-\lambda}$.

Table 8 shows the sizes of the public key, secret key, maximum signature and average signature for all the proposed parameter sets.

Set	Variant	Public key	Secret key	Signature (max)	Signature (avg)
	fast			7,877	6,151
Ia	short	190	16	$5,\!673$	4,661
Ia	shorter	129	10	5,036	4,256
	$\mathbf{shortest}$			4,536	3,936
	fast			9,105	6,668
Ib	short	144	16	6,309	4,894
10	shorter	144	10	$5,\!491$	4,406
	$\mathbf{shortest}$			4,886	4,051
	fast	205		17,139	13,372
III.	short		24	12,440	10,294
ma	shorter	205		10,746	9,131
	$\mathbf{shortest}$			9,954	$8,\!679$
	fast			18,459	13,839
IIIh	short	205	24	$13,\!136$	10,512
1110	shorter			11,202	9,236
	$\mathbf{shortest}$			10,314	8,762
	fast			31,468	23,888
Va	short	959	20	21,795	17,738
va	shorter	200	32	19,393	16,254
	$\mathbf{shortest}$			17,522	15,107
	fast			34,059	25,006
Vh	short	974	20	$23,\!182$	18,337
Vb	shorter	274	32	20,394	$16,\!663$
	$\mathbf{shortest}$			18,292	$15,\!422$

Table 8: Public keys, secret key, and signature sizes of MIRITH-HYPERCUBE in bytes.

7.3.2 Runtime

As for MIRITH, we provide an optimized implementation using AVX2 for MIRITH-HYPERCUBE. The key generation, signature generation, and verification benchmarks were performed on an 11th Gen Intel(R) Core(TM) i7-11850H @ 2.50GHz (Turbo Boost disabled). The reported timings are the medians and are shown in Table 9, where they are given in CPU cycles.

Sot	Variant	Key		Varification		
Set	variant	generation	Offline	Online	Total	Vernication
Ia	fast short shorter shortest	108,967	5,014,715 39,008,720 453,105,917 6,105,561,868	$\begin{array}{rrrr} 2,231,369 & (30.79\%) \\ 2,211,987 & (5.37\%) \\ 2,387,676 & (0.52\%) \\ 2,555,425 & (0.04\%) \end{array}$	$7,246,084 \\41,220,707 \\455,493,593 \\6,108,117,293$	6,061,955 40,976,634 456,564,597 6,195,562,217
Ib	fast short shorter shortest	202,734	6,965,263 39,624,870 456,056,236 6,104,832,926	$\begin{array}{rrrr} 2,497,293 & (26.39\%) \\ 2,461,270 & (5.85\%) \\ 2,615,400 & (0.57\%) \\ 2,755,892 & (0.05\%) \end{array}$	9,462,556 42,086,140 458,671,636 6,107,588,818	$7,914,458 \\42,047,669 \\450,442,537 \\6,040,512,181$
IIIa	fast short shorter shortest	247,821	$\begin{array}{c} 10,651,817\\ 65,262,560\\ 717,628,079\\ 9,855,053,596\end{array}$	$\begin{array}{rrrrr} 4,920,028 & (31.60\%) \\ 4,989,171 & (7.10\%) \\ 5,477,766 & (0.76\%) \\ 5,987,719 & (0.06\%) \end{array}$	$\begin{array}{c} 15,\!571,\!845\\ 70,\!251,\!731\\ 723,\!105,\!845\\ 9,\!861,\!041,\!315\end{array}$	$\begin{array}{r} 13,030,031\\ 67,659,810\\ 708,260,445\\ 9,651,788,738\end{array}$
IIIb	fast short shorter shortest	369,388	$\begin{array}{c} 13,121,055\\ 66,628,946\\ 721,516,599\\ 9,872,024,719\end{array}$	$\begin{array}{rrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrrr$	$\begin{array}{c} 18,384,614\\ 71,813,403\\ 727,245,043\\ 9,878,242,731\end{array}$	$\begin{array}{r} 15,\!550,\!479 \\ 75,\!999,\!541 \\ 732,\!036,\!291 \\ 10,\!063,\!176,\!060 \end{array}$
Va	fast short shorter shortest	521,018	$\begin{array}{c} 23,230,321\\ 109,614,396\\ 1,280,920,702\\ 16,697,326,803\end{array}$	$\begin{array}{rrrr} 10,014,703 & (30.12\%) \\ 8,879,212 & (7.49\%) \\ 10,065,728 & (0.78\%) \\ 10,718,196 & (0.06\%) \end{array}$	33,245,024 118,493,608 1,290,986,430 16,708,044,999	$\begin{array}{c} 28,269,718\\ 113,191,871\\ 1,272,158,929\\ 16,535,614,637\end{array}$
Vb	fast short shorter shortest	709,957	$\begin{array}{c} 29,363,137\\ 128,369,712\\ 1,348,619,656\\ 17,453,659,114\end{array}$	$\begin{array}{ccc} 11,302,559 & (27.79\%) \\ 10,127,974 & (7.31\%) \\ 10,848,403 & (0.80\%) \\ 11,355,820 & (0.07\%) \end{array}$	$\begin{array}{c} 40,665,696\\ 138,497,686\\ 1,359,468,059\\ 17,465,014,934\end{array}$	34,718,714 138,624,970 1,278,699,748 16,113,973,548

Table 9: Median time (in clock cycles) for the AVX2 implementation of MIRITH-HYPERCUBE on an 11th Gen Intel(R) Core(TM) i7-11850H @ 2.50GHz (Turbo Boost disabled).

We also provide an optimized implementation using the NEON instruction set for MIRITH-HYPERCUBE. The key generation, signature generation, and verification benchmarks were performed on an Apple M1 Max chip. The reported timings are the medians and are shown in Table 10, where they are given in CPU cycles.

Set	Variant	Key	Signing			Varifaction
		generation	Offline	Online	Total	vernication
Ia	fast	83,870	2,857,835	$1{,}276{,}938 \ (30.88\%)$	4,134,773	$3,\!353,\!098$
	short		22,725,008	1,241,771 (5.18%)	$23,\!966,\!779$	$23,\!119,\!796$
	shorter		$251,\!449,\!323$	1,346,319 $(0.53%)$	252,795,642	$249,\!284,\!476$
	shortest		$3,\!120,\!765,\!642$	1,381,872 (0.04%)	$3,\!122,\!147,\!514$	3,066,303,314
	fast	158,960	$3,\!850,\!299$	1,407,544 (26.77%)	$5,\!257,\!843$	4,383,691
њ	short		$23,\!875,\!525$	1,370,267 (5.43%)	$25,\!245,\!792$	$24,\!238,\!069$
ID	shorter		255,023,842	1,493,766 (0.58%)	$256,\!517,\!608$	$254,\!888,\!937$
	shortest		$3,\!183,\!130,\!055$	1,502,461 (0.05%)	$3,\!184,\!632,\!516$	$3,\!156,\!445,\!671$
	fast	000 500	6,307,315	2,671,595 (29.75%)	8,978,910	8,218,136
	short		$38,\!294,\!963$	2,752,720 (6.71%)	41,047,683	40,022,357
ma	shorter	200,552	403, 325, 122	3,198,090 (0.79%)	406,523,212	$403,\!676,\!867$
	shortest		$5,\!145,\!953,\!903$	3,366,899 (0.07%)	$5,\!149,\!320,\!802$	$5,\!120,\!266,\!642$
	fast	315,414	7,292,093	2,890,753 (28.39%)	10,182,846	9,078,766
TTTL	short		39,895,520	2,896,027 (6.77%)	42,791,547	42,146,649
IIID	shorter		414,978,504	3,227,023 (0.77%)	418,205,527	415,146,141
	shortest		$5,\!275,\!047,\!923$	3,381,685 (0.06%)	$5,\!278,\!429,\!608$	$5,\!250,\!561,\!031$
3.7	fast	408,548	12,080,451	5,330,531 (30.62%)	17,410,982	14,759,906
	short		$64,\!592,\!058$	4,785,141 (6.90%)	69,377,199	$67,\!830,\!786$
va	shorter		786,861,350	5,765,282 (0.73%)	792,626,632	800,576,501
	shortest		9,724,295,156	5,988,836 $(0.06%)$	9,730,283,992	9,799,923,065
Vb	fast	602 400	15,187,771	6,049,044 (28.48%)	21,236,815	18,167,108
	short		77,288,217	5,397,998 (6.53%)	82,686,215	$80,\!615,\!500$
	shorter	002,409	$792,\!358,\!024$	5,959,268 $(0.75%)$	798,317,292	800,096,979
	shortest		9,760,548,061	6,134,286 (0.06%)	9,766,682,347	9,811,165,545

Table 10: Median time (in clock cycles) for the NEON-ARM implementation of MIRITH-HYPERCUBE on an Apple M1 Max chip.

7.3.3 Memory

An analysis of the maximum memory usage of MIRITH-HYPERCUBE, performed using the massif tool of valgrind, is reported in Table 11. As for the MIRITH version, we did not perform the memory analysis on our Apple machine.

Set	Variant	Key generation	Signing	Verification
Ia	fast short shorter shortest	11,344	$\begin{array}{r} 86,\!040\\ 225,\!496\\ 1,\!777,\!816\\ 21,\!045,\!208\end{array}$	$\begin{array}{c} 29,752\\ 37,688\\ 221,624\\ 3,169,760\end{array}$
Ib	fast short shorter shortest	20,144	$\begin{array}{r} 107,\!288\\ 245,\!880\\ 1,\!798,\!424\\ 21,\!065,\!688\end{array}$	$\begin{array}{r} 41,080\\ 47,992\\ 231,768\\ 3,179,776\end{array}$
IIIa	fast short shorter shortest	23,136	$185,560 \\ 501,400 \\ 3,892,408 \\ 47,345,912$	$\begin{array}{r} 61,112\\ 70,072\\ 344,664\\ 4,767,136\end{array}$
IIIb	fast short shorter shortest	34,104	$\begin{array}{c} 209,560\\ 524,696\\ 3,915,064\\ 47,368,984\end{array}$	$74,096 \\82,360 \\356,920 \\4,779,232$
Va	fast short shorter shortest	46,376	341,560 877,048 7,095,256 84,164,344	$113,528 \\118,456 \\483,776 \\6,379,800$
Vb	fast short shorter shortest	64,272	380,504 915,160 7,134,328 84,203,416	$136,344 \\138,712 \\504,408 \\6,399,072$

Table 11: Memory usage (in bytes) for the AVX2 implementation of MIRITH-HYPERCUBE. Benchmarks were done on a 12th Gen Intel(R) Core(TM) i5-1240P.

8 Advantages and limitations

One of the key aspects of MIRITH is that it is constructed from a zero-knowledge proof of knowledge via the Fiat–Shamir transform. This approach inherits many advantages, but on the other hand, also bears some limitations.

Security In contrast to trapdoor-constructions, MIRITH is based on random instances of the MinRank problem. Hence, there is no additional structure introduced that could be exploited by future attacks. Further, due to its many applications in cryptanalysis, the MinRank problem is well-established in cryptographic applications and its hardness is well-understood.

Simplicity While the general framework of MPCitH introduces some overhead, our scheme breaks down into simple components. The used MPC protocol is based on a linear secret-sharing scheme, making input transformations and operations on shares straightforward. Moreover, at the lower level, our scheme relies only on standard linear algebra computations, making it easy to understand and implement.

Efficiency As MIRITH relies on the lower level only on linear-algebra computations on rather small matrices over finite fields, it is easy to optimize. However, the general ZKPoK and MPCitH context involves a lot of hashing operations, especially for computing commitments, which might become the bottleneck at some point.

Signature + Public Key Size As a ZKPoK construction, MIRITH offers very competitive signature sizes. Further, it uses very small public keys, especially after applying the outlined compression technique. The signature + public key metric, therefore, is mainly a signature size metric for MIRITH. However, again, inherent to the construction is a certain amount of commitments to be exchanged. Therefore, even by compressing problem related objects as far as possible, signatures can not become arbitrarily small.

Extension to ring signature A *ring signature scheme* enables a user to sign a message so that a *ring* of possible signers (of which the user is a member) is identified, without revealing exactly which member of that ring actually generated the signature [BKM09]. MIRITH, as any MinRankbased scheme, can be easily extended to a ring signature scheme (see, e.g., [BESV22, Section 5] for the details).

9 Security Proofs

9.1 Definitions

For the sake of completeness, we list below some common cryptographic definitions which will be used in the subsequent security proofs.

Definition 1 (Collision-Resistant Hash Function). A function $H : \{0,1\}^* \to \{0,1\}^{p(\lambda)}$, with $p(\lambda)$ polynomially-bounded by λ , is a *collision-resistant hash function* if it can be computed in polynomial time and for every probabilistic polynomial algorithm \mathcal{A} there exists a negligible function ε such that

$$\Pr\left|(x_1, x_2) \leftarrow \mathcal{A}(1^{\lambda}, H) : x_1 \neq x_2, H(x_1) = H(x_2)\right| < \varepsilon(\lambda).$$

Definition 2 ((t, ε) -Indistinguishability). Two probabilistic distributions $\{D\}$ and $\{E\}$ are said to be (t, ε) -indistinguishable, where D, E, t, ε are functions of λ , if for every probabilistic algorithm \mathcal{A} running in time at most t we have that

$$\left|\Pr\left[1 \leftarrow \mathcal{A}(x) : x \leftarrow D\right] - \Pr\left[1 \leftarrow \mathcal{A}(x) : x \leftarrow E\right]\right| < \varepsilon.$$

Definition 3 (Pseudorandom Generator (PRG)). Let $G : \{0,1\}^* \to \{0,1\}^*$ be a function such that for any $s \in \{0,1\}^{\lambda}$ we have $G(s) \in \{0,1\}^{p(\lambda)}$, where $p(\lambda)$ is a polynomially-bounded. We say that G is a (t,ε) -secure pseudorandom generator if $p(\lambda) > \lambda$ and the distributions

$$\left\{G(s): s \stackrel{\$}{\leftarrow} \{0,1\}^{\lambda}\right\} \quad \text{and} \quad \left\{r \mid r \stackrel{\$}{\leftarrow} \{0,1\}^{p(\lambda)}\right\}$$

are (t, ε) -indistinguishable.

Seed Tree Pseudorandom Generator (TreePRG) A seed tree PRG is a function that constructs a binary tree on the input of the number of leaves and a root seed value in $\{0,1\}^{\lambda}$. Recursively, the seed value of each internal node is expanded using a PRG : $\{0,1\}^{\lambda} \rightarrow \{0,1\}^{2\lambda}$ to generate the seed values of its children nodes. The main advantage of having a seed tree of N leaves is to reduce the communication cost when all but one of the leaf seeds needs to be revealed. In this case, only the seeds of a set of log N nodes are revealed, instead of N - 1 as if the seeds were generated separately. For a formal definition of seed trees and their security analysis, we refer to [BKP20, Section 2.6].

We make use of a commitment scheme $\text{Com} : \{0,1\}^* \times \{0,1\}^\lambda \to \{0,1\}^{2\lambda}$ that is assumed to be computational hiding and computational binding. We give these formal definitions below.

Definition 4 ((t, ε) -Hiding). A commitment scheme Com is (t, ε) -hiding if for every pair of messages (m_1, m_2) the following distributions are (t, ε) -indistinguishable

$$\{c: c \leftarrow \mathsf{Com}(m_1, \rho), \rho \xleftarrow{\$} \{0, 1\}^{\lambda}\}$$
 and $\{c: c \leftarrow \mathsf{Com}(m_2, \rho), \rho \xleftarrow{\$} \{0, 1\}^{\lambda}\}.$

Definition 5 (Computational binding). A commitment scheme Com is *computational binding* if for every algorithm \mathcal{A} running in a time polynomial in λ we have that

$$\Pr[\mathsf{Com}(m_1,\rho_1) = \mathsf{Com}(m_2,\rho_2) : (m_1,\rho_1,m_2,\rho_2) \leftarrow \mathcal{A}(1^{\lambda})| < \varepsilon(\lambda)]$$

where ε is a negligible function.

9.2 MPC protocol

Proposition 2. If $M_{\alpha}^{L} = M_{\alpha}^{R} \cdot K$ then Π_{s} always outputs accept. If $M_{\alpha}^{L} \neq M_{\alpha}^{R} \cdot K$ then Π_{s} outputs accept with probability at most $1/q^s$.

Proof. The proof is similar to that of [Fen22, Lemma 1], but we include it for completeness. First, we have that

$$S = \sum_{i=1}^{N} \llbracket S \rrbracket_{i} = \sum_{i=1}^{N} \left(R \cdot \llbracket \mathbf{M}_{\alpha}^{R} \rrbracket_{i} + \llbracket A \rrbracket_{i} \right) = R \cdot \mathbf{M}_{\alpha}^{R} + A.$$

Consequently, also using the fact that $C = A \cdot K$, we get that

$$V = \sum_{i=1}^{N} \llbracket V \rrbracket_{i} = \sum_{i=1}^{N} \left(S \cdot \llbracket K \rrbracket_{i} - R \cdot \llbracket \mathbf{M}_{\alpha}^{L} \rrbracket_{i} - \llbracket C \rrbracket_{i} \right)$$
$$= S \cdot K - R \cdot \mathbf{M}_{\alpha}^{L} - C = \left(R \cdot \mathbf{M}_{\alpha}^{R} + A \right) \cdot K - R \cdot \mathbf{M}_{\alpha}^{L} - A \cdot K$$
$$= R \cdot \left(\mathbf{M}_{\alpha}^{R} \cdot K - \mathbf{M}_{\alpha}^{L} \right).$$

Hence, if $M_{\alpha}^{L} = M_{\alpha}^{R} \cdot K$ then Π_{s} always outputs **accept**. In contrast, suppose that $M_{\alpha}^{L} \neq M_{\alpha}^{R} \cdot K$. Let $\Delta := M_{\alpha}^{L} - M_{\alpha}^{R} \cdot K$. Then, by the Rouché–Capelli theorem, the number of matrices $R \in \mathbb{F}_{q}^{s \times m}$ such that $R \cdot \Delta = \mathbf{0}$ is equal to $q^{(m-\mathsf{Rank}(\Delta^{\intercal}))s}$. Since $\Delta \neq 0$, we have that Rank $(\Delta^{\intercal}) \geq 1$. Consequently, the probability that Π_s outputs accept is at most $1/q^s$.

ZKPoK for MinRank 9.3

Theorem 1 (Completeness). The ZKPoK of Fig. 3 is complete. That is, an honest prover will always convince an honest verifier.

Proof. The completeness follows easily from the first part of Proposition 2.

Theorem 2 (Soundness). Assume that the commitment scheme Com is binding and that the hash function Hash₁ is collision-resistant. Then the ZKPoK of Fig. 3 is sound with soundness error

$$\varepsilon := \frac{1}{q^s} + \left(1 - \frac{1}{q^s}\right) \cdot \frac{1}{N}.$$
(7)

Precisely, suppose that there exists an efficient prover $\tilde{\mathcal{P}}$ that, on input M, convinces an honest verifier V to accept with probability $\tilde{\varepsilon} > \varepsilon$. Then there exists an efficient probabilistic extractor algorithm \mathcal{E} that, given rewindable black-box access to $\tilde{\mathcal{P}}$, produces $(\boldsymbol{\alpha}, K)$ such that $M_{\boldsymbol{\alpha}}^{L} = M_{\boldsymbol{\alpha}}^{R} \cdot K$ by making an average number of calls to $\tilde{\mathcal{P}}$ that is upper bounded by

$$\frac{4}{\tilde{\varepsilon}-\varepsilon}\left(1+\frac{\tilde{\varepsilon}\ln 4}{\tilde{\varepsilon}-\varepsilon}\right).$$

Proof. We follow the soundness proof by Feneuil, Joux, and Rivain [FJR22]. For simplicity, we assume that the commitment scheme is perfectly binding since otherwise, if it is only computationally binding, we would have to deal with commitment collisions.

$\mathcal{E}(ilde{\mathcal{P}}, ilde{arepsilon})$				
1:	for $n = 1, 2,$ do			
2:	Run $\tilde{\mathcal{P}}$ with the honest verifier V until it obtains a valid transcript T ,			
3:	and save the randomness x that generated h_1 .			
4:	for $k = 1, 2, \dots, \left\lceil (\ln 4) / (\tilde{\varepsilon} - \varepsilon) \right\rceil \mathbf{do}$			
5:	Run $\tilde{\mathcal{P}}(x)$ with V until it obtains a valid transcript T' with $i^* \neq i^{*'}$,			
6:	and save the corresponding $(\boldsymbol{\alpha}, K)$.			
7:	$\mathbf{if} \boldsymbol{M}_{\boldsymbol{\alpha}}^{L} = \boldsymbol{M}_{\boldsymbol{\alpha}}^{R} \cdot K \mathbf{then}$			
8:	$\mathbf{return}\;(\boldsymbol{\alpha},K)$			

Figure 9: Extractor \mathcal{E} .

Let $\tilde{\mathcal{P}}$, $\tilde{\varepsilon}$, and ε be as in the statement of Theorem 2. In Fig. 9, we describe an extractor \mathcal{E} that, employing the efficient prover $\tilde{\mathcal{P}}$, finds a witness $(\boldsymbol{\alpha}, K)$ satisfying $M_{\boldsymbol{\alpha}}^{L} = M_{\boldsymbol{\alpha}}^{R} \cdot K$.

Our goal is to estimate the average number of calls that \mathcal{E} makes to $\tilde{\mathcal{P}}$ before returning. Let $\eta := (\tilde{\varepsilon} - \varepsilon)/(2\tilde{\varepsilon})$ and note that $(1 - \eta)\tilde{\varepsilon} > \varepsilon$. Denote by $\operatorname{Succ}_{\tilde{\mathcal{P}}}$ the event that $\tilde{\mathcal{P}}$ succeeds on convincing an honest verifier V. Also, let X be the random variable that samples the randomness used by $\tilde{\mathcal{P}}$ in the generation of the initial commitment h_1 . We say that an x in the sample space of X is good if we have

$$\Pr[\mathsf{Succ}_{\tilde{\mathcal{P}}} \mid X = x] \ge (1 - \eta)\tilde{\varepsilon}.$$

By the Splitting Lemma [PS00], we have that

$$\Pr[x \text{ is good} \mid X = x \text{ and } \mathsf{Succ}_{\tilde{\mathcal{P}}}] \ge \eta.$$
(8)

Assume that \mathcal{E} samples a successful transcript T with X = x, and assume that x is good. By definition, we have that

$$\Pr\left[\mathsf{Succ}_{\tilde{\mathcal{P}}} \mid X = x\right] \ge (1 - \eta)\tilde{\varepsilon} > \varepsilon > \frac{1}{N},$$

This implies there exists a successful transcript T' with X = x and $i^* \neq i^{*'}$.

Note that, in the verifications of the two transcripts T and T', the revealed shares of $[\![\alpha]\!]$ must be equal, as well as the revealed shares of $[\![K]\!]$. Otherwise, since the commitment scheme is assumed to be perfectly binding, we would have $h_1 = h'_1$, contrary to the assumption that Hash₁ is collisionresistant. Therefore, there exists a unique and well-defined witness (α, K) corresponding to the transcripts T and T'.

Let (α, K) be the witness generated by $\tilde{\mathcal{P}}$ when X = x. Let us prove that if x is good then $M_{\alpha}^{L} = M_{\alpha}^{R} \cdot K$. We proceed by proving the contraposition, that is, we assume that $M_{\alpha}^{L} \neq M_{\alpha}^{R} \cdot K$ and we prove that x is not good. Let FP denote the event that a genuine execution of the MPC protocol outputs a false positive, i.e., a zero matrix V. Then, from Proposition 2, we have that

 $\Pr[\mathsf{FP}] \leq 1/q^s$. Hence, we get that

$$\Pr\left[\mathsf{Succ}_{\tilde{\mathcal{P}}} \mid X = x\right] = \Pr\left[\mathsf{Succ}_{\tilde{\mathcal{P}}} \text{ and } \mathsf{FP} \mid X = x\right] + \Pr\left[\mathsf{Succ}_{\tilde{\mathcal{P}}} \text{ and } \overline{\mathsf{FP}} \mid X = x\right]$$
$$\leq \frac{1}{q^s} + \left(1 - \frac{1}{q^s}\right) \cdot \Pr\left[\mathsf{Succ}_{\tilde{\mathcal{P}}} \mid X = x \text{ and } \overline{\mathsf{FP}}\right]. \tag{9}$$

Note that the event $\operatorname{Succ}_{\tilde{P}}$ requires that the sharing [V] in the first response of the prover must encode a zero matrix, while the event $\overline{\mathsf{FP}}$ implies that a genuine execution outputs a non-zero matrix V. Hence, to have a successful transcript, the prover must cheat for the simulation of at least one party. If the prover cheats for several parties, there is no way it can produce a successful transcript. If the prover cheats for exactly one party (among the N parties), the probability of being successful is at most 1/N. Therefore, we have that

$$\Pr\left[\mathsf{Succ}_{\tilde{\mathcal{P}}} \mid X = x \text{ and } \overline{\mathsf{FP}}\right] \leq \frac{1}{N},$$

which together with (9) yields

$$\Pr\left[\operatorname{Succ}_{\tilde{\mathcal{P}}} \mid X = x\right] \leq \frac{1}{q^s} + \left(1 - \frac{1}{q^s}\right) \cdot \frac{1}{N} = \varepsilon,$$

that is, x is not good, as desired.

If x is good, then the probability that the k-th iteration of the inner loop of Fig. 9 finds a transcript T' is

$$\begin{split} &\Pr\bigl[\mathsf{Succ}_{\tilde{\mathcal{P}}} \text{ and } i^* \neq i^{*\prime} \mid X = x\bigr] = \Pr\bigl[\mathsf{Succ}_{\tilde{\mathcal{P}}} \mid X = x\bigr] - \Pr\bigl[i^* = i^{*\prime}\bigr] \\ &\geq (1 - \eta)\tilde{\varepsilon} - \frac{1}{N} \geq (1 - \eta)\tilde{\varepsilon} - \varepsilon = \frac{\tilde{\varepsilon} - \varepsilon}{2}. \end{split}$$

Therefore, the inner loop finds T' with a probability of at least

$$1 - \left(1 - \frac{\tilde{\varepsilon} - \varepsilon}{2}\right)^{\lceil (\ln 4)/(\tilde{\varepsilon} - \varepsilon)\rceil} \ge 1 - \exp\left(\left(\ln 2\right) \ln\left(1 - \frac{\tilde{\varepsilon} - \varepsilon}{2}\right)/\frac{\tilde{\varepsilon} - \varepsilon}{2}\right) > 1 - e^{-\ln 2} = \frac{1}{2},$$

where we used the inequality $\ln(1-x)/x < -1$, which holds for $x \in (0,1)$.

Now we can give an upper bound for the average number of calls that \mathcal{E} makes to $\tilde{\mathcal{P}}$ before terminating. First, \mathcal{E} makes an average number of $1/\tilde{\varepsilon}$ calls to obtain a valid transcript T. Then, in light of (8), the probability that x is good is at least η . Moreover, by the previous consideration, if x is good then \mathcal{E} finds T' with probability at least 1/2 after $\lceil (\ln 4)/(\tilde{\varepsilon}-\varepsilon) \rceil$ calls to \tilde{P} . Therefore, the probability that T' is found is at least $\eta/2$.

Putting all together, we get that the average number of calls of the extractor \mathcal{E} to $\tilde{\mathcal{P}}$ is upperbounded by

$$\left(\frac{1}{\tilde{\varepsilon}} + \left\lceil \frac{\ln 4}{\tilde{\varepsilon} - \varepsilon} \right\rceil\right) \cdot \frac{2}{\eta} < \frac{4}{\tilde{\varepsilon} - \varepsilon} \left(1 + \frac{\tilde{\varepsilon} \ln 4}{\tilde{\varepsilon} - \varepsilon}\right).$$

The proof is complete.

Theorem 3 (Honest-Verifier Zero-Knowledge). In the ZKPoK of Fig. 3, if the PRG is (t, ε_{PRG}) -secure and the commitment scheme is (t, ε_{com}) -hiding, then there exists an efficient simulator that outputs transcripts $(t, \varepsilon_{PRG} + \varepsilon_{com})$ -indistinguishable from real transcripts of the protocol.

InternalSimulator (R, i^*) seed $\stackrel{\$}{\leftarrow} \{0,1\}^{\lambda}$, (seed_i, ρ_i)_{i \in [N]} \leftarrow TreePRG(seed) 1:For each party $i \in [N] \setminus \{i^*\}$ 2:3: $\llbracket A \rrbracket_i \leftarrow \operatorname{PRG}(\operatorname{seed}_i)$ if $i \neq N$ then 4: $\llbracket \boldsymbol{\alpha} \rrbracket_i, \llbracket K \rrbracket_i, \llbracket C \rrbracket_i \leftarrow \operatorname{PRG}(\operatorname{seed}_i), \quad \operatorname{state}_i \leftarrow \operatorname{seed}_i$ 5:else 6: $(\llbracket \pmb{\alpha} \rrbracket_N, \llbracket K \rrbracket_N, \llbracket C \rrbracket_N) \xleftarrow{\$} \mathbb{F}_q^k \times \mathbb{F}_q^{r \times (n-r)} \times \mathbb{F}_q^{s \times (n-r)}$ 7: 8: $\mathsf{aux} \leftarrow (\llbracket \alpha \rrbracket_N, \llbracket K \rrbracket_N, \llbracket C \rrbracket_N), \quad \mathsf{state}_N \leftarrow (\mathsf{seed}_N, \mathsf{aux})$ $\llbracket S \rrbracket_i \leftarrow R \cdot \llbracket \boldsymbol{M}_{\boldsymbol{\alpha}}^R \rrbracket_i + \llbracket A \rrbracket_i$ 9: $\llbracket V \rrbracket_i \leftarrow S \cdot \llbracket K \rrbracket_i - R \cdot \llbracket \boldsymbol{M}_{\boldsymbol{\alpha}}^L \rrbracket_i - \llbracket C \rrbracket_i$ 10: $\llbracket S \rrbracket_{i^*} \stackrel{\$}{\leftarrow} \mathbb{F}_q^{s \times r}$ 11: $12: \quad \llbracket V \rrbracket_{i^*} \leftarrow -\sum_{i \neq i^*} \llbracket V \rrbracket_i$ 13: $h_2 \leftarrow \text{Hash}_2([\![S]\!]_1, [\![V]\!]_1, \dots, [\![S]\!]_N, [\![V]\!]_N)$ return $(h_2, (\text{state}_i, \rho_i)_{i \neq i^*}, [S]_{i^*})$ Simulator() 1: $R \stackrel{\$}{\leftarrow} \mathbb{F}_{a}^{s \times m}, \quad i^* \stackrel{\$}{\leftarrow} [N]$ 2: $(h_2, (\mathsf{state}_i, \rho_i)_{i \neq i^*}, [S]_{i^*}) \leftarrow \mathsf{InternalSimulator}(R, i^*)$ 3: $\operatorname{com}_i \leftarrow \operatorname{Com}(\operatorname{state}_i, \rho_i)$ for each $i \in [N] \setminus \{i^*\}$ 4: $\operatorname{com}_{i^*} \xleftarrow{\$} \{0,1\}^{2\lambda}$ 5: $h_1 \leftarrow \operatorname{Hash}_1(\operatorname{com}_1, \ldots, \operatorname{com}_N)$ 6: $\mathsf{rsp} \leftarrow ((\mathsf{state}_i, \rho_i)_{i \neq i^*}, \mathsf{com}_{i^*}, [S]_{i^*})$ return (h_1, R, h_2, i^*, rsp)

Figure 10: HVZK simulator.

Proof. As in the proof of soundness, we follow the approach by Feneuil, Joux, and Rivain [FJR22]. In Fig. 10, we describe a simulator Simulator that we will prove that outputs transcripts that are $(t, \varepsilon_{\text{PRG}} + \varepsilon_{\text{com}})$ -indistinguishable from real transcripts of the protocol.

We begin by considering the subroutine InternalSimulator and by proving that its responses are $(t, \varepsilon_{\text{PRG}})$ -indistinguishable from the responses of an honest prover for the same challenges (R, i^*) . In order to do so, we describe the following sequence of simulators.

Simulator 1 (Actual protocol). This simulator, described in Fig. 11, outputs h_2 , (state_i, ρ_i)_{$i \neq i^*$}, and $[S]_{i^*}$ from the transcript of a genuine execution of the protocol with a prover that knowns a witness (α , K) and receives a challenge (R, i^*).

Simulator 2. This simulator is identical to Simulator 1, except that for party i^* it uses true randomness instead of seed-derived randomness. Precisely, $[\![A]\!]_{i^*}$ is generated by true randomness, and if $i^* \neq N$ then $[\![\alpha]\!]_{i^*}$, $[\![K]\!]_{i^*}$, $[\![C]\!]_{i^*}$ are generated by true randomness. It is easy to see that the probability of distinguishing Simulator 2 and Simulator 1 in running time t is no more than ε_{PRG} .

InternalSimulator₁(α , K, R, i^{*}) seed $\stackrel{\$}{\leftarrow} \{0,1\}^{\lambda}$, (seed_i, ρ_i)_{i \in [N]} \leftarrow TreePRG(seed) 1: For each party $i \in [N]$ 2: $\llbracket A \rrbracket_i \leftarrow \operatorname{PRG}(\operatorname{seed}_i)$ 3: if $i \neq N$ then 4: $[\![\alpha]\!]_i, [\![K]\!]_i, [\![C]\!]_i \leftarrow \operatorname{PRG}(\operatorname{seed}_i), \quad \operatorname{state}_i \leftarrow \operatorname{seed}_i$ 5:6: $\llbracket \boldsymbol{\alpha} \rrbracket_N \leftarrow \boldsymbol{\alpha} - \sum_{i \neq N} \llbracket \boldsymbol{\alpha} \rrbracket_i, \quad \llbracket K \rrbracket_N \leftarrow \boldsymbol{\alpha} - \sum_{i \neq N} \llbracket K \rrbracket_i, \quad \llbracket C \rrbracket_N \leftarrow A \cdot K - \sum_{i \neq N} \llbracket C \rrbracket_i$ 7: $\mathsf{aux} \leftarrow (\llbracket \boldsymbol{\alpha} \rrbracket_N, \llbracket K \rrbracket_N, \llbracket C \rrbracket_N), \quad \mathsf{state}_N \leftarrow (\mathsf{seed}_N, \mathsf{aux})$ 8: $\llbracket S \rrbracket_i \leftarrow R \cdot \llbracket \boldsymbol{M}_{\boldsymbol{\alpha}}^R \rrbracket_i + \llbracket A \rrbracket_i$ 9: $\llbracket V \rrbracket_i \leftarrow S \cdot \llbracket K \rrbracket_i - R \cdot \llbracket \boldsymbol{M}_{\boldsymbol{\alpha}}^L \rrbracket_i - \llbracket C \rrbracket_i$ 10: 11: $h_2 \leftarrow \text{Hash}_2([S]_1, [V]_1, \dots, [S]_N, [V]_N)$ return $(h_2, (\text{state}_i, \rho_i)_{i \neq i^*}, [S]_{i^*})$

Figure 11: InternalSimulator₁ on input the witness (α, K) and a challenge (R, i^*) .

Simulator 3. This simulator is identical to Simulator 2, but $[\![\alpha]\!]_N$, $[\![K]\!]_N$, $[\![C]\!]_N$ are generated by true randomness and $[\![V]\!]_{i^*} = -\sum_{i \neq i^*} [\![V]\!]_i$. In particular, the output does not depend anymore on the witness (α, K) , and Simulator 3 takes as input only the challenges (R, i^*) .

Now we have to prove that the distributions of the outputs of Simulator 2 and Simulator 3 are in fact identical.

If $i^* = N$, then the changes only impact the shares $[\![S]\!]_N$, $[\![V]\!]_N$. Since the additive term $[\![A]\!]_N$ is sampled uniformly at random in Simulator 2, and it does not change in Simulator 3, we have that the distribution of $[\![S]\!]_N$ does not change. Moreover, since $[\![V]\!]_N = -\sum_{i \neq N} [\![V]\!]_i$ already in Simulator 2, its distribution does not change in Simulator 3.

If $i^* \neq N$, then the changes impact first $\mathsf{aux} = (\llbracket \alpha \rrbracket_N, \llbracket K \rrbracket_N, \llbracket C \rrbracket_N)$, then $\llbracket S \rrbracket_N$ and $\llbracket V \rrbracket_N$, since they are derived from aux , and finally $\llbracket V \rrbracket_{i^*}$, since it has the additive term $\llbracket V \rrbracket_N$. However, aux was already uniformly random in Simulator 2. Indeed, the shares in aux are computed by adding share values from parties $i \neq N$, including the i^* party (which is uniformly random in Simulator 2). Therefore, the output distributions of Simulator 2 and Simulator 3 are identical.

Simulator 4 (Internal HVZK simulator). This is InternalSimulator in Fig. 10. The only difference with Simulator 3 is that $[S]_{i^*}$ is drawn uniformly at random. As explained before, this does not impact the output distribution.

Thus we have shown that InternalSimulator outputs responses that are (t, ε_{PRG}) -indistinguishable from the responses of the real protocol on the same challenges of an honest verifier. Applying the hiding property of the commitment scheme on com_{i^*} , we obtain that Simulator in Fig. 10 outputs a transcript that is $(t, \varepsilon_{PRG} + \varepsilon_{com})$ -indistinguishable from a real transcript of the protocol.

9.4 Unforgeability

Theorem 4. Suppose that PRG is (t, ε_{PRG}) -secure and that any adversary running in time t has at most an advantage ε_{MR} against the underlying MinRank problem associated with a public key

M. Moreover, assume that Hash_0 , Hash_1 , and Hash_2 are modeled as random oracles. Let \mathcal{A} be an adaptive chosen-message adversary against the signature scheme described in Fig. 5, running in time t, making q_s signing queries, and q_i queries to Hash_i for i = 0, 1, 2. Then \mathcal{A} succeeds in outputting a valid forgery with probability

$$\Pr[\mathsf{Forge}] \le \frac{3(q_0 + q_1 + q_2 + \tau N q_s)^2}{2^{2\lambda + 1}} + \frac{q_s(q_s + q_0 + q_1 + q_2)}{2^{2\lambda}} + q_s \tau \varepsilon_{\mathrm{PRG}} + \varepsilon_{\mathrm{MR}} + \max_{0 \le t \le \tau} P(t),$$
(10)

where

$$P(t) := \left(1 - \left(1 - P_b(t)\right)^{q_1}\right) \left(1 - \left(1 - P_{a,c}(t)\right)^{q_2}\right),$$
$$P_b(t) := \left(\frac{1}{q^s}\right)^t \left(1 - \frac{1}{q^s}\right)^{\tau - t} \binom{\tau}{t},$$
$$P_{a,c}(t) := \frac{1}{N^{\tau - t}}.$$

Proof. In the following, we will define a sequence of experiments involving \mathcal{A} . We let $\Pr_i[\cdot]$ denote the probability of an event in the *i*th experiment. Let *t* denote the running time of the entire experiment (including \mathcal{A} 's running time, the time required to answer signing queries, and the time to verify \mathcal{A} 's output).

Experiment 1. This is the interaction of \mathcal{A} with the real signature scheme. Precisely, first KeyGen is run to obtain \mathcal{M}, α, K . Then \mathcal{A} is given the public key \mathcal{M} and access to the signing oracle, which returns a valid signature under the key (α, K) for any requested message, and to the random oracles Hash_i for i = 0, 1, 2. At the end of this experiment, \mathcal{A} outputs a message-signature pair (msg, σ). We let Forge denote the event that msg was not previously queried by \mathcal{A} to the signing oracle and that σ is a valid signature for msg.

Our goal is to upper-bound $Pr_1[Forge]$.

Experiment 2. We proceed as Experiment 1 with the only exception that we abort if, during the course of the experiment, a collision of Hash₀, Hash₁, or Hash₂ is found. The number of queries to Hash₀, Hash₁, or Hash₂, by either the adversary or the signing oracle, is at most $q_0 + q_1 + q_2 + \tau N q_s$. Therefore, using the classic bound for the probability of a collision of a hash function⁵, we have that

$$\left|\Pr_{1}[\mathsf{Forge}] - \Pr_{2}[\mathsf{Forge}]\right| < \frac{3(q_{0} + q_{1} + q_{2} + \tau N q_{s})^{2}}{2^{2\lambda + 1}}$$

Experiment 3. We proceed as Experiment 2 with the only exception that we abort if, while answering a signature query, the salt sampled in the signature query is equal to a salt used in a previous query (by \mathcal{A} or by another signing query) to Hash₀, Hash₁ or Hash₂. For each signature query, the probability to abort is at most $(q_s + q_0 + q_1 + q_2)/2^{2\lambda}$. Therefore, we have that

$$\left|\Pr_{2}[\mathsf{Forge}] - \Pr_{3}[\mathsf{Forge}]\right| \leq \frac{q_{s}(q_{s} + q_{0} + q_{1} + q_{2})}{2^{2\lambda}}.$$

⁵The probability that n calls to a hash functions $H : \{0, 1\}^* \to \{0, 1\}^{2\lambda}$ lead to a collision is at most $n(n-1)/2^{2\lambda+1}$.

Experiment 4. The difference with Experiment 3 is that, when signing a message msg, in Phases 2 and 4 of the signing algorithm (see Fig. 5) h_1 and h_2 are set to be uniformly random values in $\{0,1\}^{2\lambda}$, without making the corresponding queries to Hash₁ and Hash₂.

Hence, the outcome of Experiment 3 differs from this one only if, in the course of answering a signing query, it happens that the underlying query to Hash_1 or Hash_2 was made before by \mathcal{A} or by a signing query. But this cannot happen, since in such a case Experiment 3 would abort. Thus, we have that

$$\Pr_4[\mathsf{Forge}] = \Pr_3[\mathsf{Forge}]$$

Experiment 5. The difference with Experiment 4 is that, for each $\ell \in [\tau]$, we set $\operatorname{com}_{i^{*},(\ell)}^{(\ell)}$ to be a uniformly random value in $\{0,1\}^{2\lambda}$, without making the corresponding query to Hash₀.

Hence, the outcome of Experiment 4 differs from this one only if, in the course of answering a signing query, Hash_0 receives an input that was previously queried. Note that such an event cannot occur within the same signing query, since the indices i and ℓ are part of the input to Hash_0 , and it cannot occur from a previous query (signing query or \mathcal{A} query), because in such a case Experiment 3 would abort. Therefore, we have that

$$\Pr_5[\mathsf{Forge}] = \Pr_4[\mathsf{Forge}]$$

\mathcal{O}^{Sig}	$\mathcal{O}^{Sign\prime}(msg)$		
	Phase 0.		
1:	$salt \stackrel{\$}{\leftarrow} \{0,1\}^{2\lambda}, h_1 \stackrel{\$}{\leftarrow} \{0,1\}^{2\lambda}, h_2 \stackrel{\$}{\leftarrow} \{0,1\}^{2\lambda}$		
2:	$R^{(1)}, \ldots, R^{(\tau)} \leftarrow \operatorname{PRG}(h_1)$ where $R^{(\ell)} \in \mathbb{F}_q^{s \times m}$.		
3:	$i^{*,(1)}, \ldots, i^{*,(\tau)} \leftarrow \operatorname{PRG}(h_2)$ where $i^{*,(\ell)} \in [N]$.		
	Phases 1 and 3.		
4:	For each $\ell \in [\tau]$:		
5:	$\left(\left(state_i^{(\ell)}\right)_{i \neq i^*, (\ell)}, \left([\![S^{(\ell)}]\!]_i, [\![V^{(\ell)}]\!]_i\right)_{i \in [N]}\right) \leftarrow InternalSimulator_{salt}(R^{(\ell)}, i^{*, (\ell)})$		
6:	$com_i^{(\ell)} \leftarrow \mathrm{Hash}_0(salt, \ell, i, state_i^{(\ell)}) \text{ for each } i \neq i^*.$		
7:	$com_{i^*,(\ell)}^{(\ell)} \stackrel{\$}{\leftarrow} \{0,1\}^{2\lambda}$		
	Phases 2 and 4.		
8:	Set $\operatorname{Hash}_1(\operatorname{msg}, \operatorname{salt}, (\operatorname{com}_i^{(\ell)})_{i \in [N], \ell \in [\tau]})$ equal to h_1 (i.e., if \mathcal{A} makes		
	subsequent queries to Hash_1 with the same input, then return h_1 as output).		
9:	Set $\operatorname{Hash}_2(\operatorname{msg},\operatorname{salt},(\llbracket S^{(\ell)} \rrbracket_i, \llbracket V^{(\ell)} \rrbracket_i)_{i \in [N], \ell \in [\tau]})$ equal to h_2 (i.e., if \mathcal{A} makes		
	subsequent queries to Hash_2 with the same input, then return h_2 as output).		
	Phase 5: Assembling the signature.		
10:	$\sigma \leftarrow \left(salt, h_1, h_2, \left(\left(state_i^{(\ell)}\right)_{i \neq i^*, (\ell)}, com_{i^*, (\ell)}^{(\ell)}, [\![S^{(\ell)}]\!]_{i^*, (\ell)}\right)_{\ell \in [\tau]}\right)$		
	return σ		

Figure 12: Signing query for a message msg in Experiment 6.

Experiment 6. This experiment is obtained by modifying Experiment 5 as follows. For $\ell \in [\tau]$, the signer uses the code of InternalSimulator in Fig. 10 to generate the parties' views in one execution of Phases 1 and 3. We write InternalSimulator_{salt} to denote a call to this simulator that prepends salt to the sampled seed in input to TreePRG and PRG. Thus, signature queries are now answered as described in Fig. 12. Observe that the secret (α, K) is no longer used for generating signatures. Recall from the proof of Theorem 3 that an adversary against InternalSimulator_{salt} has a distinguishing advantage ε_{PRG} (corresponding to execution time t) since commitments are built outside of the simulator. Consequently, we have that

$$\left| \Pr_5[\mathsf{Forge}] - \Pr_6[\mathsf{Forge}] \right| \le q_s \tau \varepsilon_{\mathrm{PRG}}.$$

Experiment 7. At any point during this experiment, we say that we have a correct execution ℓ^* if, in a query to Hash₂ with input

$$\Big(\mathsf{msg},\mathsf{salt},h_1,\Big(\left[\!\left[S^{(\ell)}\right]\!\right]_i,\left[\!\left[V^{(\ell)}\right]\!\right]_i\Big)_{i\in[N],\ \ell\in[\tau]}\Big),$$

we have that:

- 1. there is a previous query $h_1 \leftarrow \operatorname{Hash}_1(\mathsf{msg}, \mathsf{salt}, \mathsf{com}_1^{(1)}, \ldots, \mathsf{com}_N^{(\tau)});$
- 2. each $\operatorname{com}_{i}^{(\ell^{*})}$ is the output of a previous query to Hash_{0} (by either \mathcal{A} or the signing oracle) with input $(\operatorname{salt}, \ell, i, \operatorname{state}_{i}^{(\ell^{*})})$;
- 3. and a solution $(\boldsymbol{\alpha}, K)$ to the MinRank instance \boldsymbol{M} can be extracted from $(\mathsf{state}_i^{(\ell^*)})_{i \in [N]}$.

In this experiment, in each query made by \mathcal{A} to Hash_2 (where msg was not previously queried), it is checked if there is a correct execution. We call this event Solve. Note that if Solve occurs then $(\operatorname{state}_i^{(\ell^*)})_{i \in [N]}$ (which can be determined from the oracle queries of \mathcal{A}) allow one to efficiently recover a solution (α, K) to the MinRank instance M. Thus, we have that $\Pr_6[\operatorname{Solve}] \leq \varepsilon_{\mathrm{MR}}$. Hence, we get that

 $\begin{aligned} \Pr_{6}[\mathsf{Forge}] &= \Pr_{6}[\mathsf{Forge} \text{ and } \mathsf{Solve}] + \Pr_{6}[\mathsf{Forge} \text{ and not } \mathsf{Solve}] \\ &\leq \varepsilon_{\mathrm{MR}} + \Pr_{6}[\mathsf{Forge} \text{ and not } \mathsf{Solve}]. \end{aligned}$

Now, suppose that the adversary produced a forgery (msg, σ) and Solve did not occur. In particular, it does not occur for the query to Hash₂ of the form

$$\left(\mathsf{msg},\mathsf{salt},h_1, \left(\left.\left[\!\left[S^{(\ell)}\right]\!\right]_i,\left[\!\left[V^{(\ell)}\right]\!\right]_i\right)_{i\in[N],\;\ell\in[\tau]}\right)$$

associated to the forgery (msg, σ). Since there is a forgery then the above condition 1 verifies, that is, there is a previous query $h_1 \leftarrow \text{Hash}_1(\text{msg}, \text{salt}, \text{com}_1^{(1)}, \dots, \text{com}_N^{(\tau)})$. Therefore, for each $\ell \in [\tau]$, we have that exactly one of the three following cases must occur:

- (a) $\operatorname{com}_{i^{*,\ell}}^{(\ell)}$ is not the output of a query to Hash_0 with an input of the form $(\operatorname{salt}, \ell, i, \operatorname{state}_i^{(\ell^*)})$.
- (b) for all $i \in [N]$, $\operatorname{com}_{i}^{(\ell)}$ is the output of a query to Hash_{0} with input $(\operatorname{salt}, \ell, i, \operatorname{state}_{i}^{(\ell)})$,
 - the witness (α, K) extracted from $(\mathsf{state}_i^{(\ell)})_{i \in [N]}$ is a not a solution to M,

- $\llbracket S^{(\ell)} \rrbracket_{i^*}$ and $\llbracket V^{(\ell)} \rrbracket_{i^*}$ are genuinely computed from $\mathsf{state}_{i^*}^{(\ell)}$, h_1 , and $S^{(\ell)}$.
- (c) Same as (b) but $[S^{(\ell)}]_{i^*}$ or $[V^{(\ell)}]_{i^*}$ are not genuinely computed from $\mathsf{state}_{i^*}^{(\ell)}$, h_1 , and $S^{(\ell)}$.

Clearly, if (b) occurs for a round $\ell \in [\tau]$, this means that the MPC protocol Π_s in Fig. 2 to verify matrix-multiplication triple is honestly followed by each party $i \in [N]$. Hence, from Proposition 2, we have that the adversary has probability $1/q^s$ to have (b) satisfied for this round ℓ . Therefore, the probability of having exactly $t \in [\tau]$ rounds satisfying (b) is at most

$$P_b(t) = \left(\frac{1}{q^s}\right)^t \left(1 - \frac{1}{q^s}\right)^{\tau-t} \binom{\tau}{t}.$$

If (b) does not occur for a round $\ell \in [\tau]$, this means that any other second challenge obtained from h_2 different from $i^{*,\ell}$ would make the forgery fail. Hence, the probability of having this round ℓ not leading to rejection is at most 1/N. Therefore, the probability of having exactly $\tau - t \in [\tau]$ rounds satisfying (a) or (c) is at most

$$P_{a,c}(t) = \frac{1}{N^{\tau-t}}.$$

In view of the above, the probability of having Forge and not Solve with exactly t rounds satisfying (b) after q_1 queries to Hash₁ and q_2 queries to Hash₂ is at most

$$P(t) = \left(1 - \left(1 - P_b(t)\right)^{q_1}\right) \left(1 - \left(1 - P_{a,c}(t)\right)^{q_2}\right).$$

Thus, we have

$$\Pr_6[\mathsf{Forge and not Solve}] \le \max_{0 \le t \le \tau} P(t).$$

The final upper bound (10) follows by putting together all the bounds for the probabilities given in the experiments. $\hfill \Box$

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